

DJ-191

Service Manual

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SPECIFICATIONS

	TX	RX
Frequency Coverage		
DJ-191T (U.S. Amateur version)	144.000 ~ 147.995MHz	135.000 ~ 173.995MHz
DJ-191E (European Amateur version)	144.000 ~ 145.995MHz	144.000 ~ 145.995MHz
DJ-191TA1 (Commercial version VHFL)	135.000 ~ 155.000MHz	135.000 ~ 173.995MHz
DJ-191TA2 (Commercial version VHFH)	150.000 ~ 173.995MHz	135.000 ~ 173.995MHz
Channel Step:	5, 10, 12.5, 15, 20, 25, 30kHz steps	
Memory Channels:	40 Channels + 1 Call Channel	Memory
Antenna Impedance:	50Ω unbalanced	
Frequency Stability:	± 5 ppm	
Microphone Input Impedance:	2kΩ nominal.	
Signal Type:	F3E (FM)	
Offset Range:	0 ~ 99.995MHz	
Deviation:	± 5kHz max.	
TX Output (supply voltage):	1.5W (4.8V) / 3.5W (7.2V) / 5W (9.6 ~ 13.8V)	
RX Sensitivity:	12dB SINAD better than - 16dB μ	
RX Selectivity:	- 6dB / ± 12kHz	
I.F.:	(1st) 21.25MHz / (2nd) 450kHz	
Power Supply Requirements:	4.8 ~ 13.8V DC (4.8V DC standard)	
Current Consumption at 13.8V DC:	Transmitting: Approx. 1.2 Amp. in High Power Setting	
Operating Temperature:	Receiving: Squelched Approx. 24mA (BS on) - 10 ~ + 60°C, 14 ~ 140°F	
Dimensions: (with EBP-37N without projections)	57(W) × 151(H) × 28(D) mm 2 ¹ / ₄ (W) × 6(H) × 1 ¹ / ₁₆ (D) inches	
Weight:	Approx. 300g	
DTMF:	16 Button Keypad, encoder/decoder installed	
Subaudible Tones (CTCSS):	Encoder installed (50 tones)	

CIRCUIT DESCRIPTION

1) Receiver System

The receiver system is a double superheterodyne system with a 21.7 MHz first IF and a 450 kHz second IF.

1. Front End

The received signal at any frequency in the 130.00- to 173.995-MHz range is passed through the low-pass filter (L102, L103, L104, C113, C107, C116, and C114) and tuning circuit (L112 and D107), and amplified by the RF amplifier (Q107). The signal from Q107 is then passed through the tuning circuit (L109, L110, L111, and varicaps D104, D105 and D106) and converted into 21.7 MHz by the mixer (Q106). The tuning circuit, which consists of L112, L109, varicaps D107 and D104, L110, L111, varicaps D105 and D106, is controlled by the tracking voltage from the CPU so that it is optimized for the reception frequency. The local signal from the VCO is passed through the buffer (Q108), and supplied to the source of the mixer (Q106). The radio uses the lower side of the superheterodyne system.

2. IF Circuit

The mixer mixes the received signal with the local signal to obtain the sum of and difference between them. The crystal filter (XF101, XF102) selects 21.7 MHz frequency from the results and eliminates the signals of the unwanted frequencies. The first IF amplifier (Q105) then amplifies the signal of the selected frequency.

3. Demodulator Circuit

After the signal is amplified by the first IF amplifier (Q105), it is input to pin 16 of the demodulator IC (IC104). The second local signal of 21.25 MHz (shared with PLL IC reference oscillation), which is oscillated by the internal oscillation circuit in IC102 and crystal (X101), is input through pin 1 of IC104. Then, these two signals are mixed by the internal mixer in IC104 and the result is converted into the second IF signal with a frequency of 450 kHz. The second IF signal is output from pin 3 of IC104 to the ceramic filter (FL101), where the unwanted frequency band of that signal is eliminated, and the resulting signal is sent back to the IC104 through pins 5 and 7.

The second IF signal input via pin 7 is demodulated by the internal limiter amplifier and quadrature detection circuit in IC104, and output as an audio signal through pin 9.

4. Audio Circuit

The audio signal from pin 9 of IC104 is compensated to the audio frequency characteristics in the de-emphasis circuit (R162, R161, C172, C173) and amplified by the AF amplifier (Q109). The signal is then input to pin 2 of the electronic volume (IC103) for volume adjustment, and output from pin 1. The adjusted signal is sent to the audio power amplifier (IC105) through pin 2 to drive the speaker.

5. Squelch Circuit

Part of the audio signal from pin 9 of IC104 is amplified by the noise filter amplifier consisting of R176, R186, R177, C179, C183, C191, and C194, and the internal noise amplifier in IC104. The desired noise of the signal is output through pin 11 of IC104, to be further amplified by the noise amplifier (Q115). The amplified noise signal is rectified by voltage doubler D109 and input to pin 4 of CPU (IC5).

2) Transmitter System

1. Modulator Circuit

The audio signal is converted to an electric signal in either the internal or external microphone, and input to the microphone amplifier (IC6). IC6 consists of two operational amplifiers; one amplifier (pins 1, 2, and 3) is composed of pre-emphasis and IDC circuits and the other (pins 5, 6, and 7) is composed of a splatter filter. The maximum frequency deviation is obtained by VR2 and input to the cathode of the varicap of the VCO, to change the electric capacity in the oscillation circuit. This produces the frequency modulation.

2. Power Amplifier Circuit

The transmitted signal is oscillated by the VCO, amplified by the pre-drive amplifier (Q102) and drive amplifier (Q101), and input to the power module (IC101). The signal is then amplified by the power module (IC101) and led to the antenna switch (D101) and low-pass filter (L102, L103, L104, C113, C107, C116, and C114), where unwanted high harmonic waves are reduced as needed, and the resulting signal is supplied to the antenna.

3. APC Circuit

Part of the transmission power from the low-pass filter is detected by D103, converted to DC, and then amplified by a differential amplifier. The output voltage controls the bias voltage from pin 2 of the power module (IC101) to maintain the transmission power constant.

3) PLL Synthesizer Circuit

1. PLL

The dividing ratio is obtained by sending data from the CPU (IC5) to pin 2 and sending clock pulses to pin 3 of the PLL IC (IC102). The oscillated signal from the VCO is amplified by the buffer (Q117) and input to pin 6 of IC102. Each programmable divider in IC102 divides the frequency of the input signal by N according to the frequency data, to generate a comparison frequency of 5 or 6.25 kHz.

2. Reference Frequency Circuit

The reference frequency appropriate for the channel steps is obtained by dividing the 21.25 MHz reference oscillation (X101) by 4250 or 3400, according to the data from the CPU (IC5). When the resulting frequency is 5 kHz, channel steps of 5, 10, 15, 20, 25, 30, and 50 kHz are used. When it is 6.25 kHz, the 12.5 kHz channel step is used.

3. Phase Comparator Circuit

The PLL (IC102) uses the reference frequency, 5 or 6.25 kHz. The phase comparator in the IC102 compares the phase of the frequency from the VCO with that of the comparison frequency, 5 or 6.25 kHz, which is obtained by the internal divider in IC102.

4. PLL Loop Filter Circuit

If a phase difference is found in the phase comparison between the reference frequency and VCO output frequency, the charge pump output (pin 8) of IC102 generates a pulse signal, which is converted to DC voltage by the PLL loop filter and input to the varicap of the VCO unit for oscillation frequency control.

5. VCO Circuit

A Colpitts oscillation circuit driven by Q301 directly oscillates the desired frequency. The frequency control voltage determined in the CPU (IC5) and PLL circuit is input to the varicaps (D301 and D304). This changes the oscillation frequency, which is amplified by the VCO buffer (Q302) and output from the VCO unit.

Note

The oscillation frequency is determined by turning Q301 ON and OFF.

Displayed frequencies	Q301
TX: 130.00 - 139.995 MHz RX: 130.00 - 161.695 MHz	OFF
TX: 140.00 - 173.995 MHz RX: 161.70 - 173.995 MHz	ON

4) CPU and Peripheral Circuits

1. LCD Display Circuit

The CPU turns ON the LCD via segment and common terminals with 1/3 the duty and 1/3 the bias, at the frame frequency is 85Hz.

2. Display Lamp Circuit

When the LAMP key is pressed, "H" is output from pin 45 of CPU (IC5) to the bases of Q1 and Q12. Q1 and Q12 then turn ON and the LEDs (D1, D3, D14, D15, D16, and D17) light.

3. Reset and Backup Circuits

When the power from the DC jack or external battery increases from 0 V to 2.5 or more, "H" level reset signal is output from the reset IC (IC2) to pin 35 of the CPU (IC5), causing the CPU to reset. The reset signal, however, waits at C6 and R1010, and does not enter the CPU until the CPU clock (X1) has stabilized. When the external power drops to 3.2 V or below, the output signal from the backup IC (IC3), which has been input to pin 34 of the CPU, changes from "H" to "L" level. The CPU will then be in the backup state.

4. S(Signal)Meter Circuit

The DC potential of pin 13 of IC104 is input to pin 3 of the CPU (IC5), converted from an analog to a digital signal, and displayed as the S-meter signal on the LCD.

5. DTMF Encoder

The CPU (IC5) is equipped with an internal DTMF encoder. The DTMF signal is output from pin 12, through R90 and R91 (for level adjustment), and then through the microphone amplifier (IC6), and is sent to the varicap of the VCO for modulation. At the same time, the monitoring tone passes through the AF circuit and is output from the speaker.

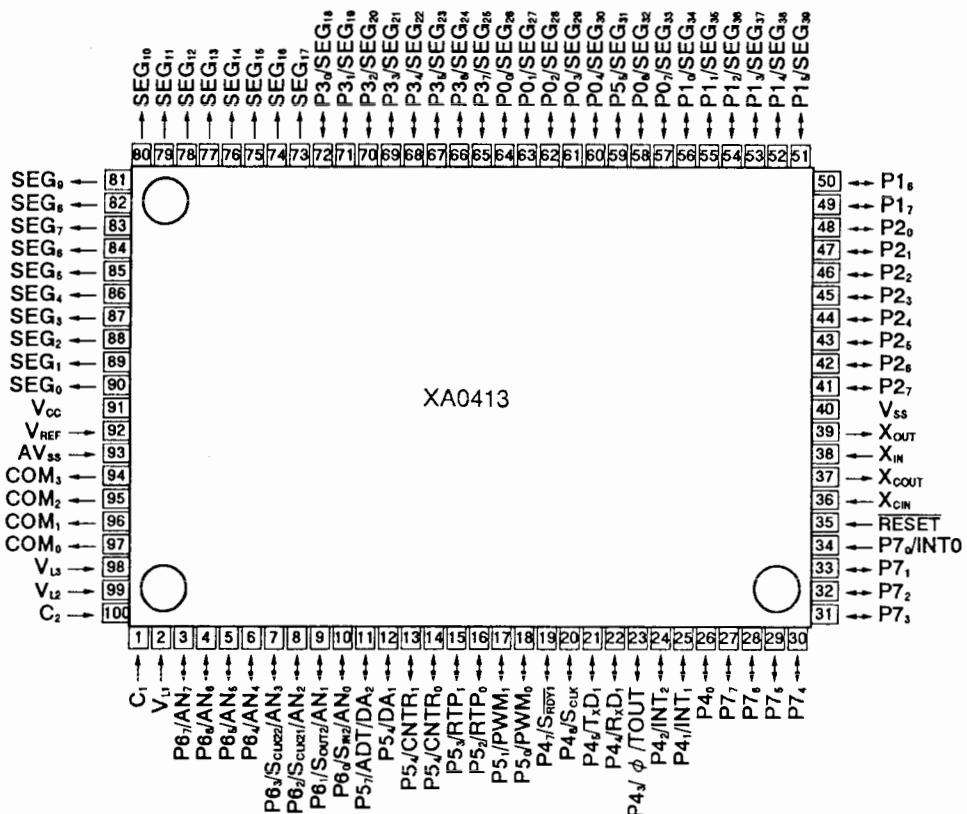
6. DTMF Decoder

Part of the audio signal demodulated by IC104 is input to pin 1 of DTMF IC (IC8). The internal signal judging circuit in IC 8 then checks if the signal is valid or invalid. The judged signal is converted into a 4-bit code and sent to pin 29 of IC5.

7. Tone Encoder

The CPU (IC5) is equipped with an internal tone encoder. The tone signal (67.0 to 254.1 Hz) is output from pin 11 of the CPU to the varicap of the VCO for modulation.

5) CPU Terminal Functions: M38267M8L (XA0413)



No.	Pin Name	Signal	I/O	Logic	Description
1	C1	C1	-	-	-
2	VL1	VL1	I	A/D	LCD power supply
3	P67/AN7	SMT	I	A/D	S-meter input
4	P66/AN6	SQL	I	A/D	Noise level input for squelch
5	P65/AN5	BAT	I	A/D	Low battery detection input
6	P64/AN4	BP5	I	A/D	Band plan 5
7	P63/CLK22/AN3	BP4	I	-	Band plan 4
8	P62/CLK21/AN2	UL	I	Active high	PLL unlock signal input
9	P61/SOUT2/AN1	BP1,2	I	A/D	Band plans 1 and 2
10	P60/SIN2/AN0	MONI	I	Active low	Monitor key input
11	P57/ADT/DA2	CTOUT	O	D/A	CTCSS tone output
12	P56/AD1	DTOUT	O	D/A	DTMF output
13	P55/CNTR1	TSQD	I	Active low	CTCSS tone detection input/Trunking board detection
14	P54/CNTR0	BEP	O	Pulse	Beep tone output/Band plan 3
15	P53/RTP1	STB2	I/O	Active low/pulse	CTCSS unit detection/Strobe signal to CTCSS unit/Strobe signal to trunking board/Audio line control
16	P52/RTP0	MUTE	I/O	Active high	Microphone mute/Bank change input while trunking
17	P51/PWM1	CLK	O	Pulse	Serial clock output for PLL, CTCSS, and trunking board
18	P50/PWM0	DATA	O	Pulse	Serial data output for PLL, CTCSS, and trunking board
19	P47/SRDY1	ACK	I/O	Pulse	Clock output for DTMF shift out/Band plan 6
20	P46/SCLK1	STB1	O	Pulse	Strobe for PLL IC
21	P45/TXD1	UTX	O	Pulse	UART data transmission output
22	P44/RXD1	URX	I	Pulse	UART data reception input
23	P43/φ/TOUT	TBST	O	Pulse	Tone burst (1750Hz) output (European version)
24	P42/INT2	RE2	I	Active low	Rotary encoder input
25	P41/INT1	RE1	I	Active low	
26	P40	PTT	I	Active high	PTT input
27	P77	DSW	O	Active low	DTMF IC ON/OFF
28	P76	STD	I/O	Active high	DTMF signal detection input during reception/Deviation adjustment during transmission
29	P75	DSD	I	Pulse	Decoded DTMF serial data input during reception/Deviation adjustment during transmission
30	P74	T3C	O	Active low	TX power ON/OFF output
31	P73	P3C	O	Active low	PLL power ON/OFF output
32	P72	AFP	O	Active low	AFAMP power ON/OFF output
33	P71	R3C	O	Active low	RX power ON/OFF output
34	P70/INTO	BU	I	Active low	Backup signal detection input
35	RESET	RST	I	Active low	Reset input
36	XCIN	XCIN	-	-	-
37	XCOUNT	XCOUT	-	-	-
38	XIN	XIN	-	-	Main clock input
39	XOUT	XOUT	-	-	Main clock output
40	VSS	GND	-	-	CPU ground
41	P27	PSW	I	Active low	Power switch input
42	P26	SCL	O	Pulse	Serial clock for EEPROM
43	P25	C3C	O	Active high	C3 power ON/OFF output
44	P24	SDA	O	Pulse	Serial data for EEPROM
45	P23	LMP	O	Active high	Lamp ON/OFF
46	P22	T/KEY	I	Active low	Tone burst/LPTT input
47	P21	K00	I/O	-	Key matrix output/Band plan BP7 input
48	P20	K01	O	-	Key matrix output
49	P17	K02	O	-	
50	P16	K03	O	-	

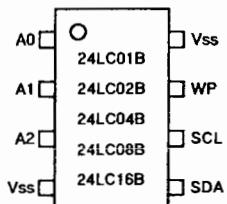
No.	Pin Name	Signal	I/O	Logic	Description
51	P15/SEG39	F/KEY	I	Active low	Function key input Key matrix input
52	P14/SEG38	K10	I	-	
53	P13/SEG37	K11	I	-	
54	P12/SEG36	K12	I	-	
55	P11/SEG35	K13	I	-	
56	P10/SEG34	K14	I	-	
57	P07/SEG33	SFT	O	-	VCO frequency range change
58	P06/SEG32	SD	O	Active low	Signal detection output
59	P05/SEG31	AFC	O	Active high	AF tone control output
60	P04/SEG30	DA4	O	-	DA converter for electronic volume and output power
61	P03/SEG29	DA3	O	-	
62	P02/SEG28	DA2	O	-	
63	P01/SEG27	DA1	O	-	
64	P00/SEG26	DA0	O	-	
65	P37/SEG25	S25	O	-	
66	P36/SEG24	S24	O	-	LCD segment signal
67	P35/SEG23	S23	O	-	
68	P34/SEG22	S22	O	-	
69	P33/SEG21	S21	O	-	
70	P32/SEG20	S20	O	-	
71	P31/SEG19	S19	O	-	
72	P30/SEG18	S18	O	-	
73	SEG17	S17	O	-	
74	SEG16	S16	O	-	
75	SEG15	S15	O	-	
76	SEG14	S14	O	-	
77	SEG13	S13	O	-	
78	SEG12	S12	O	-	
79	SEG11	S11	O	-	
80	SEG10	S10	O	-	
81	SEG9	S9	O	-	
82	SEG8	S8	O	-	
83	SEG7	S7	O	-	
84	SEG6	S6	O	-	
85	SEG5	S5	O	-	
86	SEG4	S4	O	-	
87	SEG3	S3	O	-	
88	SEG2	S2	O	-	
89	SEG1	S1	O	-	
90	SEG0	S0	O	-	
91	VCC	VDD	-	-	CPU power terminal
92	VREF	VREF	-	-	AD converter power supply
93	AVSS	AVSS	-	-	AD converter ground
94	COM3	COM3	-	-	-
95	COM2	COM2	O	-	LCD COM2 output
96	COM1	COM1	O	-	LCD COM1 output
97	COM0	COM0	O	-	LCD COM0 output
98	VL3	VL3	I	-	LCD power supply
99	VL2	VL2	I	-	LCD power supply
100	C2		I	-	-

SEMICONDUCTOR DATA

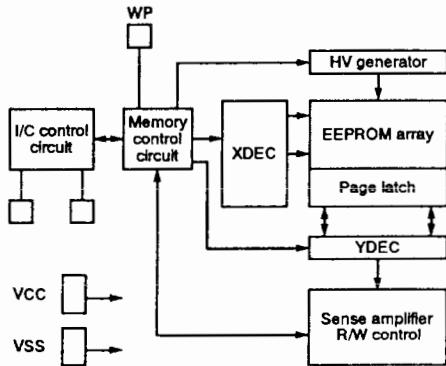
1) 24LC16BT-I/SN (XA0351)

EEPROM

Pin Assignment



Block Diagram



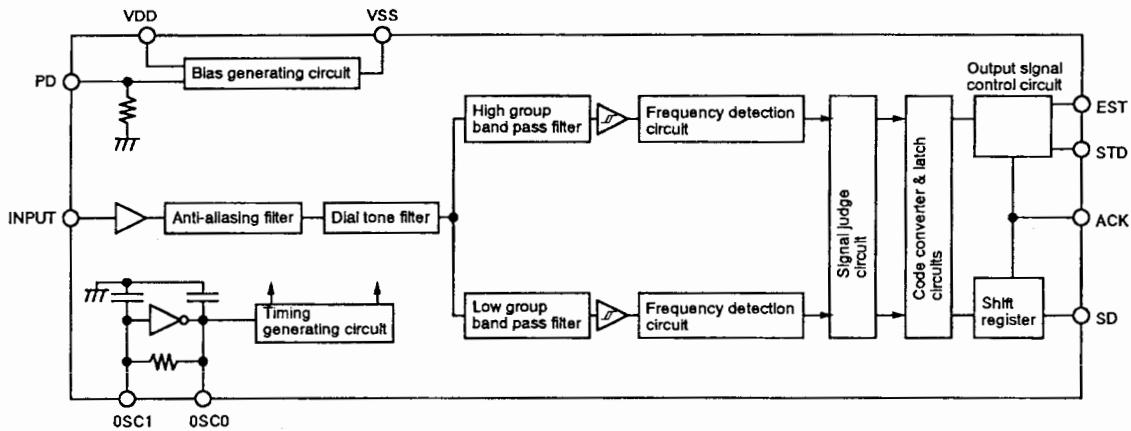
2) LC73881M-TLM (XA0344)

DTMF Receiver

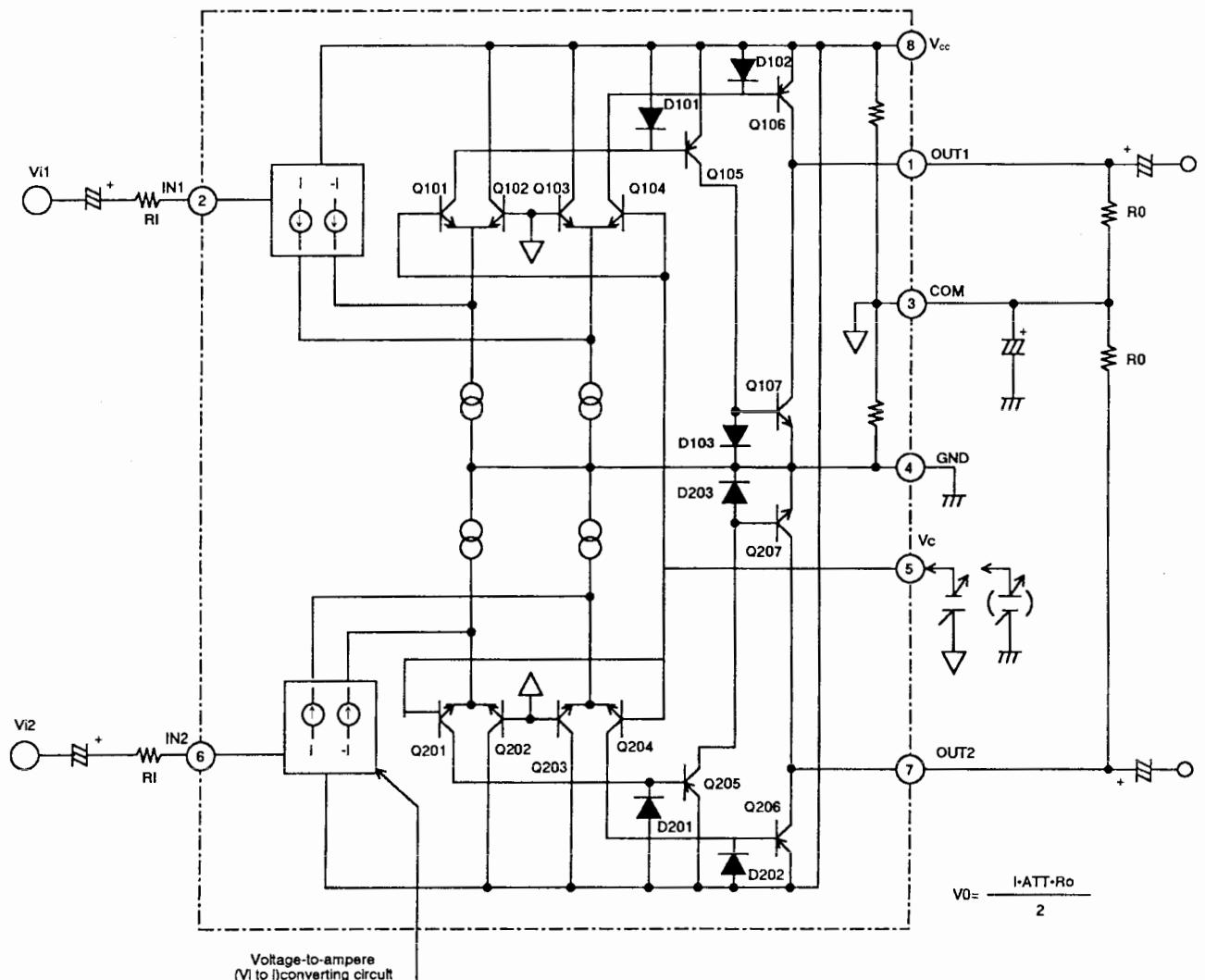
Pin Functions

Pin nos.	Signal	I/O	Description
1	INPUT	I	An input coupling capacitor is required. This input signal is internally biased by the V DD/2.
2	PD	I	When this signal goes HIGH, the system enters the power-down mode.
3	OSCO	O	These lines are connected to a crystal oscillator or a ceramic resonator of 194,304 MHz to form the oscillation circuit.
4	OSCI	I	
5	VSS	-	Power terminal (usually 0V).
6	SD	O	The decoded DTMF data is output as serial 4-bit data, starting with the LSB.
7	ACK	I	The ACK signal is used to shift out the data to pin 2 (PD). Four pulses are required to shift out a four-bit DTMF code. The leading edge of the first pulse latches the data into the shift register before shifting out.
8	STD	O	This signal goes HIGH when a DTMF code is sent. This signal changes LOW to HIGH slower than the EST signal, however the burst frequency for this signal uses a dead band.
9	EST	O	This signal goes HIGH when a DTMF code is sent. This line is externally monitored to determine an appropriate time, and then four pulses are input to the ACK terminal to allow the SD terminal to output the DTMF data.
10	VDD	-	Power terminal (usually, 2.7 V to 5.5 V)

Block Diagram



3) M5222FP-600C (XA0385) Electronic Volume



Voltage-to-ampere
(V_i to I)converting circuit

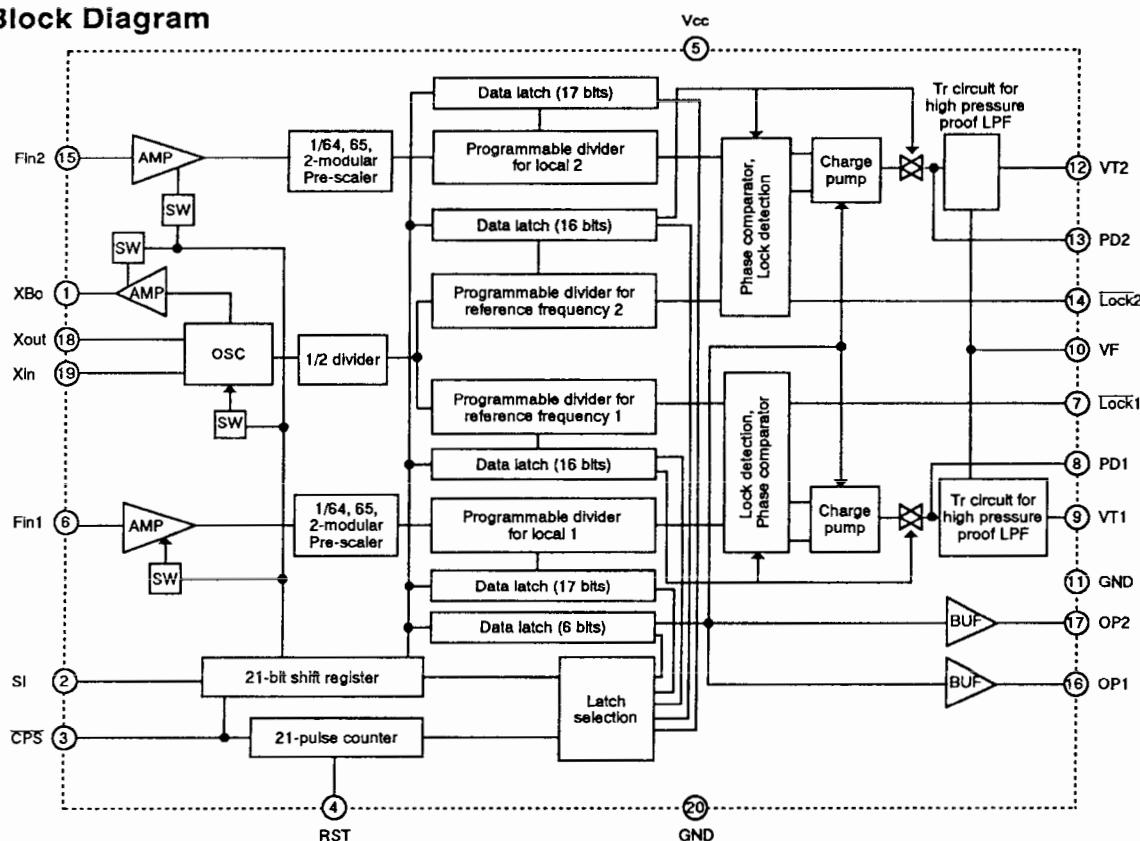
$$I = V_i / R_I$$

4) M64076GP (XA0352) PLL

Pin Assignment

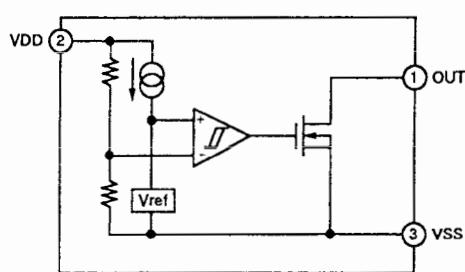
XBo	1	20	GND
SI	2	19	Xin
CPS	3	18	Xout
RST	4	17	OP2
Vcc	5	16	OP1
Fin1	6	15	Fin2
Lock1	7	14	Lock2
PD1	8	13	PD2
VT1	9	12	VT2
VF	10	11	GND

Block Diagram



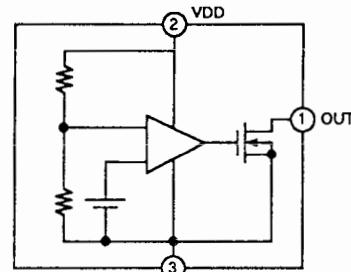
5) RH5VL25AA-T1 (XA0309) C-MOS Voltage Detector

Block Diagram



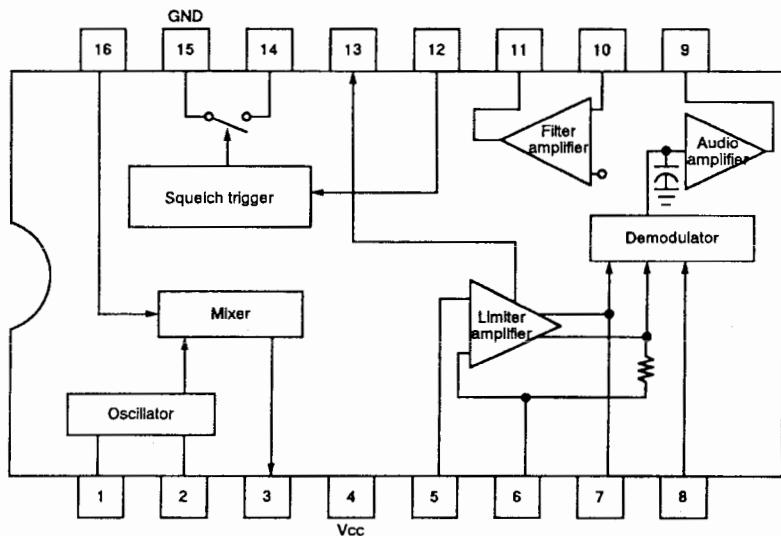
6) RH5VA32AA-T1 (XA0198) C-MOS Voltage Detector

Block Diagram

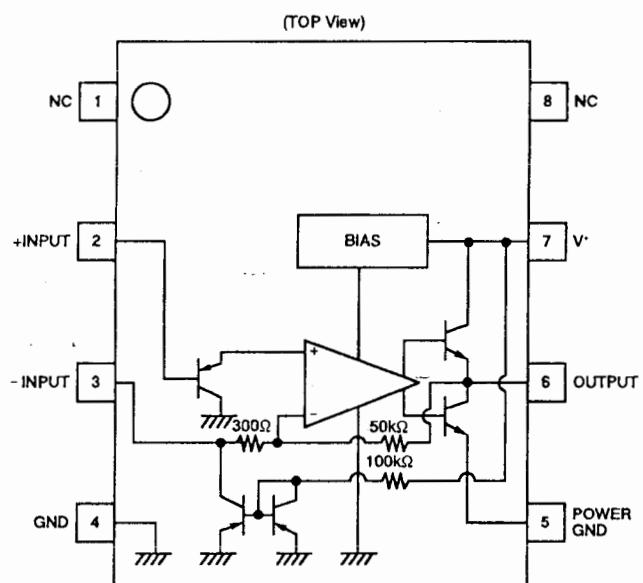


7) MC3372VM-EL (XA0343) Narrow Band FM IF IC

Block Diagram



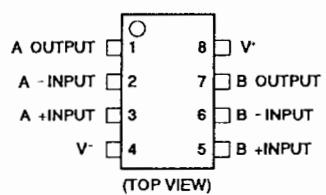
8) NJM2070M T1 (XA0210) Audio Power Amplifier



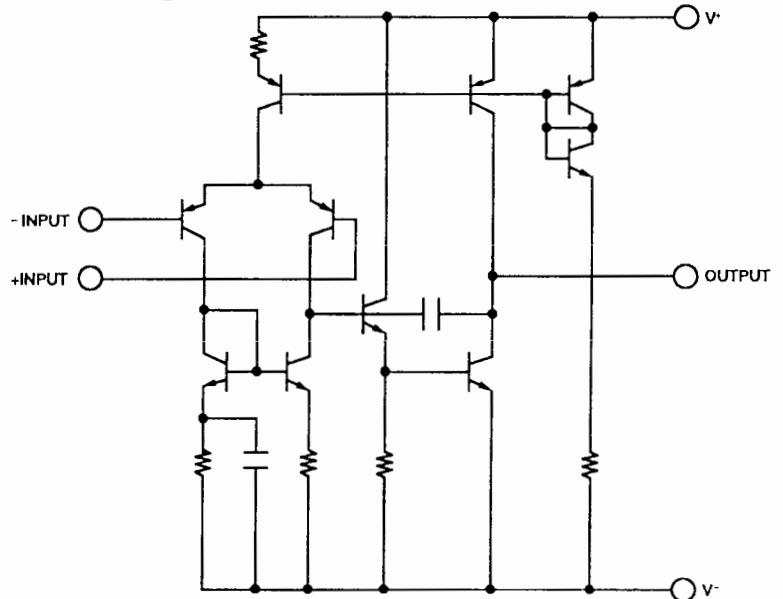
9) NJM2100M T1 (XA0209)

Operational Amplifier

Pin Assignment



Block Diagram



10) Transistor, Diode, and LED Outline Drawings

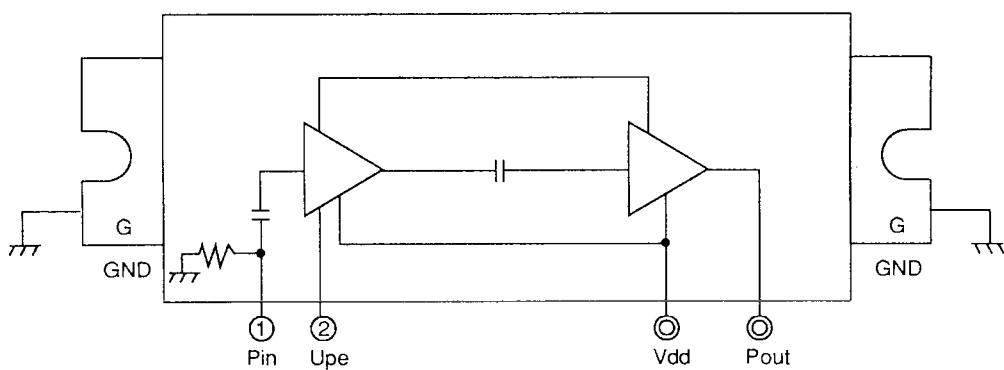
Top View

DA204U T106 XD0130	FMA7XT 148 XU0027	MA716 TW XD0118	MA741WA TX XD0251	MA742 TX XD0250
UN211H TX XU0040	UN2214 TX XU0038	UN9111 TX XU0062	XP1501 TX XU0172	

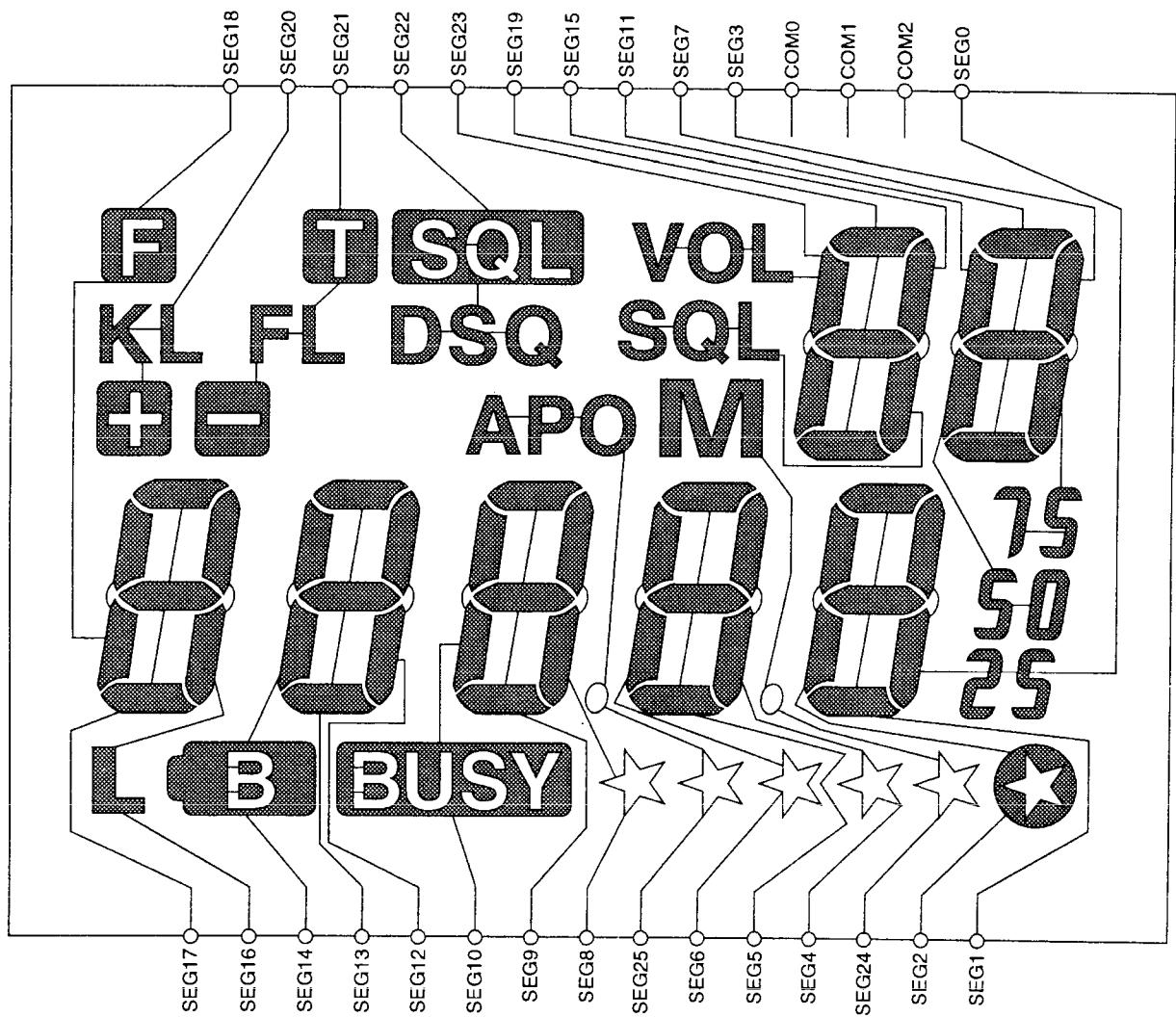
11) P. A. Module (IC101)

TA1 : XA0439
TA2 : XA0421

T : XA0381

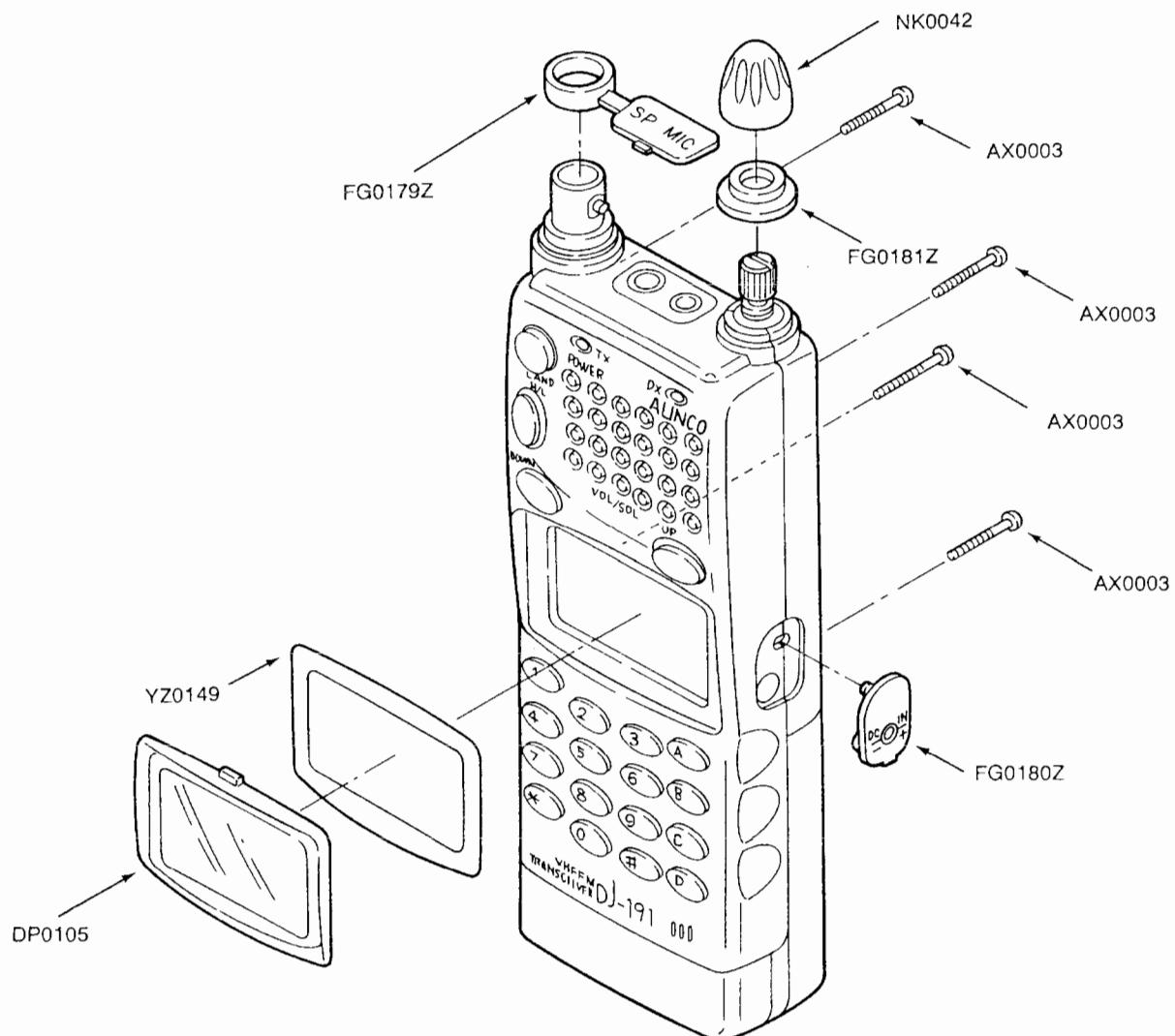


12) LCD Connection

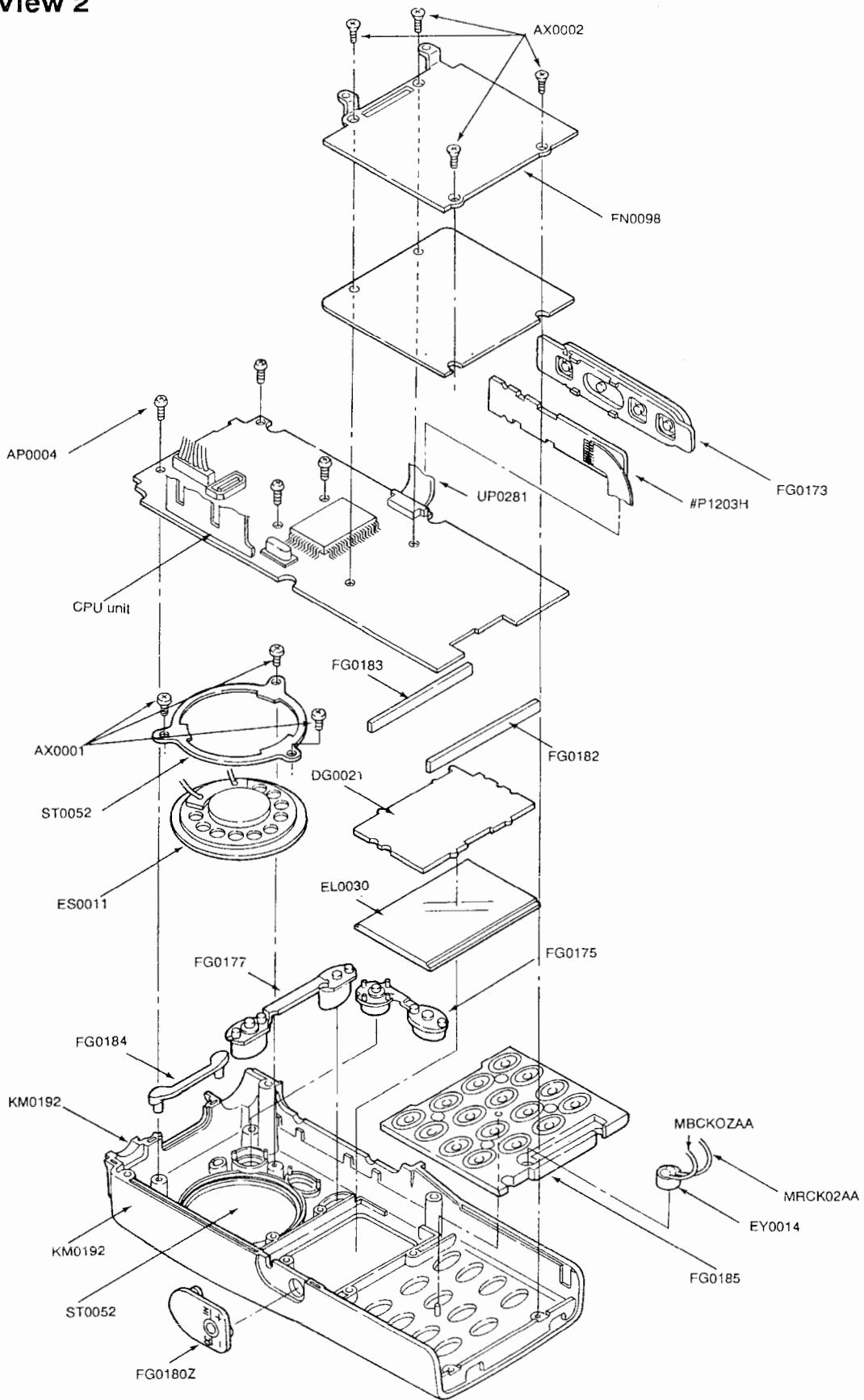


EXPLODED VIEW

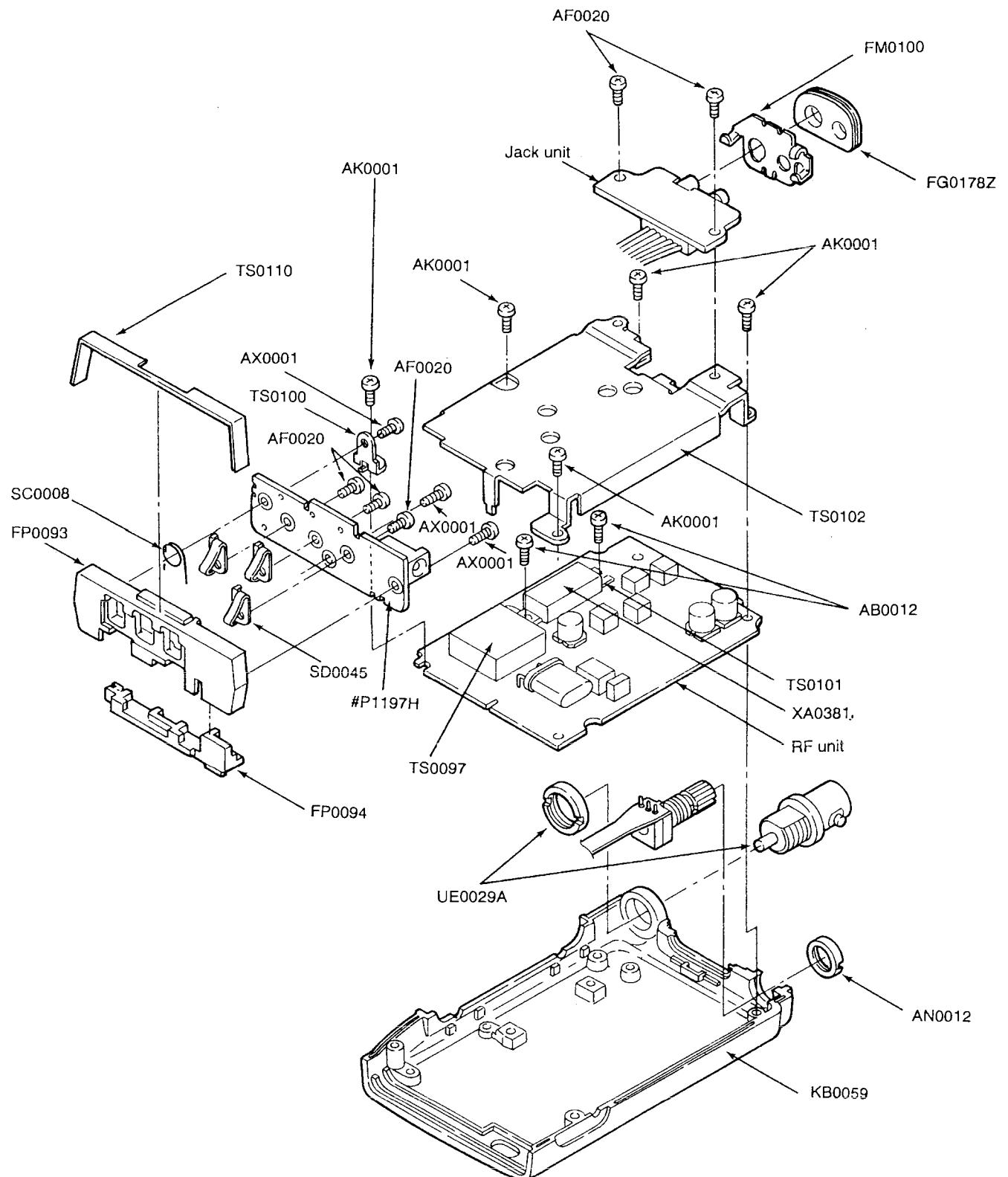
1) Front View 1



2) Front View 2



3) Rear View



PARTS LIST

Ref. No.	Parts No.	Description	Parts Name	Ver.
CPU Unit				
C1	CU3035	Chip C.	C1608JB1H10ZKTA	
C2	CU3035	Chip C.	C1608JB1H10ZKTA	
C3	CS0378	Chip Tantal	TMC1C0105MTR	
C4	CU3017	Chip C.	C1608CH1H13ZQTA	
C5	CU3017	Chip C.	C1608CH1H13ZQTA	
C6	CS0201	Chip Tantal	TMC1A0475MTR	
C7	CU3035	Chip C.	C1608JB1H10ZKTA	
C8	CU3035	Chip C.	C1608JB1H10ZKTA	
C9	CS0378	Chip Tantal	TMC1G105MTR	
C10	CS0373	Chip Tantal	TCD1A1676MTR	
C11	CS0378	Chip Tantal	TMC1C0105MTR	
C12	CS0359	Chip C.	C1608JF1H10ZKTA	
C13	CS0063	Chip Tantal	TCS1A1V10MTR	
C14	CU3047	Chip C.	C1608JB1H10ZKTA	
C15	CS0049	Chip Tantal	TMC1G105MTR	
C16	CS0057	Chip Tantal	TCS1A01225MTR	
C18	CS0049	Chip Tantal	TCS1A1C105MTR	
C19	CS0321	Chip C.	C1608CH1H8QJTA	
C20	CS0335	Chip C.	C1608JB1H10ZKTA	
C21	CU3056	Chip C.	C1608JF1E177ZTA	
C22	CS0335	Chip C.	C1608JB1H10ZKTA	
C23	CS0335	Chip C.	C1608JB1H10ZKTA	
C24	CS0351	Chip C.	C1608JR1E223KTA	
C25	CS0361	Chip C.	C1608JR1E223KTA	
C26	CS0327	Chip C.	C1608JF1E177ZTA	
C27	CS0335	Chip C.	C1608JB1H10ZKTA	
C28	CS026	Chip C.	C1608CH1H8JTA	
C29	CS0359	Chip C.	C1608CH1H21JTA	
C30	CS0359	Chip C.	C1608JF1E104ZTA	
C31	CS0063	Chip Tantal	TCS1A1V10MTR	
C32	CS0359	Chip C.	C1608CH1H105C7A	
C33	CU3035	Chip C.	C1608JB1H104ZTA	
C35	CS0359	Chip C.	C1608CH1H10ZKTA	
C37	CS0049	Chip Tantal	TMC1A105MTR	
C39	CS0359	Chip C.	C1608JF1E104ZTA	
C40	CS0306	Chip C.	C1608CH1H105C7A	
C41	CU3059	Chip C.	C1608JB1H104ZTA	
C42	CS0335	Chip C.	C1608JB1H104ZTA	
C43	CS0335	Chip C.	C1608JB1H10ZKTA	
C44	CS0335	Chip C.	C1608JB1H10ZKTA	
C45	CS0359	Chip C.	C1608JB1H10ZKTA	
C46	CS0335	Chip C.	C1608JB1H10ZKTA	
C47	CS0335	Chip C.	C1608JB1H10ZKTA	
C48	CS0335	Chip C.	C1608JB1H10ZKTA	
C49	CS0335	Chip C.	C1608JB1H10ZKTA	
C52	CS0359	Chip C.	C1608JF1E104ZTA	
CN1	UE0270	Wire	JACK-CPU Wire	
CN2	UE0256	Diode	DP1058-0201 PCB	
CN3	UP0282	Diode	DAS-005 Flexible PCB	
CN7	UE0267	LED	AMW20C330P	
D1	XL0045	LED	PG110IF-TR	
D3	XL0045	LED	PG110IF-TR	
D5	XL0047	LED	PG110IW-TR	
D6	XL0048	LED	BR110IW-TR	
D7	XD0291	Diode	MA729-TX	
D9	XD0291	Diode	MA729-TX	
D11	XD0250	Diode	MA712-TX	
D12	XD0291	Diode	MA729-TX	
D13	XD0291	Diode	MA729-TX	
D14	XL0036	LED	SML-310MTT86	
D15	XL0036	LED	SML-310MTT86	
D16	XL0036	LED	SML-310MTT86	
D17	XL0036	LED	SML-310MTT86	

Ref. No.	Parts No.	Description	Parts Name	Ver.
CPU Unit/CHARGE Unit/Mechanical Parts				
R1022	RK3035	EPP ROM	2HLIC16BT-1/5N	
R1023	RK3035	IC	RH5W125MA-11	
R1025	RK3048	IC	RH5W132AA-11	
R1027	RK3041	IC	S-812355G-Q1-T2	
R1028	RK3046	IC	M58257ML-0TP	
R1029	RK3062	IC	NM2120B-11	
R1030	RK3050	IC	LG73881M-TLM	
R1031	RK3066	IC	#30AII-025-H1	
R1032	RK3039	Wire	MACLU12GG	
R1034	RK3042	Chip L.	MLF1608A1R0KTA00	
R1035	RK3042	Chip L.	MLF1608A1R0KTA00	
R1036	RK3042	Chip L.	MLF1608A1R0KTA00	
R1037	RK3042	Chip L.	MLF1608A1R0KTA00	
R1038	RK3042	Chip L.	MLF1608A1R0KTA00	
R1039	RK3042	Chip L.	MLF1608A1R0KTA00	
R1040	RK3042	Chip L.	MLF1608A1R0KTA00	
R1041	RK3042	Chip L.	MLF1608A1R0KTA00	
R1042	RK3042	Chip L.	MLF1608A1R0KTA00	
R1043	RK3042	Chip L.	MLF1608A1R0KTA00	
R1044	RK3042	Chip L.	MLF1608A1R0KTA00	
R1045	RK3042	Chip L.	MLF1608A1R0KTA00	
R1046	RK3042	Chip L.	MLF1608A1R0KTA00	
R1047	RK3042	Chip L.	MLF1608A1R0KTA00	
R1048	RK3042	Chip L.	MLF1608A1R0KTA00	
R1049	RK3042	Chip L.	MLF1608A1R0KTA00	
R1050	RK3042	Chip L.	MLF1608A1R0KTA00	
R1051	RK3042	Chip L.	MLF1608A1R0KTA00	
R1052	RK3042	Chip L.	MLF1608A1R0KTA00	
R1053	RK3042	Chip L.	MLF1608A1R0KTA00	
R1054	RK3042	Chip L.	MLF1608A1R0KTA00	
R1055	RK3042	Chip L.	MLF1608A1R0KTA00	
R1056	RK3042	Chip L.	MLF1608A1R0KTA00	
R1057	RK3042	Chip L.	MLF1608A1R0KTA00	
R1058	RK3042	Chip L.	MLF1608A1R0KTA00	
R1059	RK3042	Chip L.	MLF1608A1R0KTA00	
R1060	RK3042	Chip L.	MLF1608A1R0KTA00	
R1061	RK3042	Chip L.	MLF1608A1R0KTA00	
R1062	RK3042	Chip L.	MLF1608A1R0KTA00	
R1063	RK3042	Chip L.	MLF1608A1R0KTA00	
R1064	RK3042	Chip L.	MLF1608A1R0KTA00	
R1065	RK3042	Chip L.	MLF1608A1R0KTA00	
R1066	RK3042	Chip L.	MLF1608A1R0KTA00	
R1067	RK3042	Chip L.	MLF1608A1R0KTA00	
R1068	RK3042	Chip L.	MLF1608A1R0KTA00	
R1069	RK3042	Chip L.	MLF1608A1R0KTA00	
R1070	RK3042	Chip L.	MLF1608A1R0KTA00	
R1071	RK3042	Chip L.	MLF1608A1R0KTA00	
R1072	RK3042	Chip L.	MLF1608A1R0KTA00	
R1073	RK3042	Chip L.	MLF1608A1R0KTA00	
R1074	RK3042	Chip L.	MLF1608A1R0KTA00	
R1075	RK3042	Chip L.	MLF1608A1R0KTA00	
R1076	RK3042	Chip L.	MLF1608A1R0KTA00	
R1077	RK3042	Chip L.	MLF1608A1R0KTA00	
R1078	RK3042	Chip L.	MLF1608A1R0KTA00	
R1079	RK3042	Chip L.	MLF1608A1R0KTA00	
R1080	RK3042	Chip L.	MLF1608A1R0KTA00	
R1081	RK3042	Chip L.	MLF1608A1R0KTA00	
R1082	RK3042	Chip L.	MLF1608A1R0KTA00	
R1083	RK3042	Chip L.	MLF1608A1R0KTA00	
R1084	RK3042	Chip L.	MLF1608A1R0KTA00	
R1085	RK3042	Chip L.	MLF1608A1R0KTA00	
R1086	RK3042	Chip L.	MLF1608A1R0KTA00	
R1087	RK3042	Chip L.	MLF1608A1R0KTA00	
R1088	RK3042	Chip L.	MLF1608A1R0KTA00	
R1089	RK3042	Chip L.	MLF1608A1R0KTA00	
R1090	RK3042	Chip L.	MLF1608A1R0KTA00	
R1091	RK3042	Chip L.	MLF1608A1R0KTA00	
R1092	RK3042	Chip L.	MLF1608A1R0KTA00	
R1093	RK3042	Chip L.	MLF1608A1R0KTA00	
R1094	RK3042	Chip L.	MLF1608A1R0KTA00	
R1095	RK3042	Chip L.	MLF1608A1R0KTA00	
R1096	RK3042	Chip L.	MLF1608A1R0KTA00	
R1097	RK3042	Chip L.	MLF1608A1R0KTA00	
R1098	RK3042	Chip L.	MLF1608A1R0KTA00	
R1099	RK3042	Chip L.	MLF1608A1R0KTA00	
R1100	RK3042	Chip L.	MLF1608A1R0KTA00	
R1101	RK3042	Chip L.	MLF1608A1R0KTA00	
R1102	RK3042	Chip L.	MLF1608A1R0KTA00	
R1103	RK3042	Chip L.	MLF1608A1R0KTA00	
R1104	RK3042	Chip L.	MLF1608A1R0KTA00	
R1105	RK3042	Chip L.	MLF1608A1R0KTA00	
R1106	RK3042	Chip L.	MLF1608A1R0KTA00	
R1107	RK3042	Chip L.	MLF1608A1R0KTA00	
R1108	RK3042	Chip L.	MLF1608A1R0KTA00	
R1109	RK3042	Chip L.	MLF1608A1R0KTA00	
R1110	RK3042	Chip L.	MLF1608A1R0KTA00	
R1111	RK3042	Chip L.	MLF1608A1R0KTA00	
R1112	RK3042	Chip L.	MLF1608A1R0KTA00	
R1113	RK3042	Chip L.	MLF1608A1R0KTA00	
R1114	RK3042	Chip L.	MLF1608A1R0KTA00	
R1115	RK3042	Chip L.	MLF1608A1R0KTA00	
R1116	RK3042	Chip L.	MLF1608A1R0KTA00	
R1117	RK3042	Chip L.	MLF1608A1R0KTA00	
R1118	RK3042	Chip L.	MLF1608A1R0KTA00	
R1119	RK3042	Chip L.	MLF1608A1R0KTA00	
R1120	RK3042	Chip L.	MLF1608A1R0KTA00	
R1121	RK3042	Chip L.	MLF1608A1R0KTA00	
R1122	RK3042	Chip L.	MLF1608A1R0KTA00	
R1123	RK3042	Chip L.	MLF1608A1R0KTA00	
R1124	RK3042	Chip L.	MLF1608A1R0KTA00	
R1125	RK3042	Chip L.	MLF1608A1R0KTA00	
R1126	RK3042	Chip L.	MLF1608A1R0KTA00	
R1127	RK3042	Chip L.	MLF1608A1R0KTA00	
R1128	RK3042	Chip L.	MLF1608A1R0KTA00	
R1129	RK3042	Chip L.	MLF1608A1R0KTA00	
R1130	RK3042	Chip L.	MLF1608A1R0KTA00	
R1131	RK3042	Chip L.	MLF1608A1R0KTA00	
R1132	RK3042	Chip L.	MLF1608A1R0KTA00	
R1133	RK3042	Chip L.	MLF1608A1R0KTA00	
R1134	RK3042	Chip L.	MLF1608A1R0KTA00	
R1135	RK3042	Chip L.	MLF1608A1R0KTA00	
R1136	RK3042	Chip L.	MLF1608A1R0KTA00	
R1137	RK3042	Chip L.	MLF1608A1R0KTA00	
R1138	RK3042	Chip L.	MLF1608A1R0KTA00	
R1139	RK3042	Chip L.	MLF1608A1R0KTA00	
R1140	RK3042	Chip L.	MLF1608A1R0KTA00	
R1141	RK3042	Chip L.	MLF1608A1R0KTA00	
R1142	RK3042	Chip L.	MLF1608A1R0KTA00	
R1143	RK3042	Chip L.	MLF1608A1R0KTA00	
R1144	RK3042	Chip L.	MLF1608A1R0KTA00	
R1145	RK3042	Chip L.	MLF1608A1R0KTA00	
R1146	RK3042	Chip L.	MLF1608A1R0KTA00	
R1147	RK3042	Chip L.	MLF1608A1R0KTA00	
R1148	RK3042	Chip L.	MLF1608A1R0KTA00	
R1149	RK3042	Chip L.	MLF1608A1R0KTA00	
R1150	RK3042	Chip L.	MLF1608A1R0KTA00	
R1151	RK3042	Chip L.	MLF1608A1R0KTA00	
R1152	RK3042	Chip L.	MLF1608A1R0KTA00	
R1153	RK3042	Chip L.	MLF1608A1R0KTA00	
R1154	RK3042	Chip L.	MLF1608A1R0KTA00	
R1155	RK3042	Chip L.	MLF1608A1R0KTA00	
R1156	RK3042	Chip L.	MLF1608A1R0KTA00	
R1157	RK3042	Chip L.	MLF1608A1R0KTA00	
R1158	RK3042	Chip L.	MLF1608A1R0KTA00	
R1159	RK3042	Chip L.	MLF1608A1R0KTA00	
R1160	RK3042	Chip L.	MLF1608A1R0KTA00	
R1161	RK3042	Chip L.	MLF1608A1R0KTA00	
R1162	RK3042	Chip L.	MLF1608A1R0KTA00	
R1163	RK3042	Chip L.	MLF1608A1R0KTA00	
R1164	RK3042	Chip L.	MLF1608A1R0KTA00	
R1165	RK3042	Chip L.	MLF1608A1R0KTA00	
R1166	RK3042	Chip L.	MLF1608A1R0KTA00	

Mechanical Parts/PTT Unit/JACK Unit/VCO Unit/SW Unit

Ref. No.	Parts No.	Description	Parts Name	Ver.
PG0177	V/U Key DJG5	VCO Unit		
FG0182	LCD Rubber (A) DJG5	C301 CU3035 Chip C	C1608JB1H102KTA	C1608CH1H02KTA
FG0183	LCD Rubber (B) DJG5	C302 CU3037 Chip C	C1608JB1H102KTA	C1608CH1H1120JTA
FG0184	ON AIR Rubber DJG5	C303 CU3047 Chip C	C1608JB1H102KTA	C1608CH1H102KTA
FG0185	16-Key Rubber DJ191	C304 CU3047 Chip C	C1608JB1H102KTA	TMC5AC1C105MTR
FG0218	Cushion DJG5	C305 CU3031 Chip C	C1608JB1H102KTA	C1608JB1F1E427ZTA
JM0068	Rear Panel DJG5	C306 CU3006 Chip C	C1608CH1H050CTA	C1608JB1F1E104ZTA
ANCK033A	Lead #28N02-030-02	C307 CU3035 Chip C	C1608JB1H102KTA	C1608JB1F1E223KTA
ST0052	SP Irene DJG5	C308 CU3006 Chip C	C1608CH1H050CTA	C1608JB1H102KTA
IG0023	SP Cloth Tape DJG5	C309 CU3003 Chip C	C1608CH1H1020CTA	C1608JB1H102KTA
IG0026	Panel sheet DJG5	C310 CU3031 Chip C	C1608JB1H1047KTA	C1608JB1H1102KTA
WZ0149	LCD Tape DJG5	C311 CU3035 Chip C	C1608JB1H1050CTA	TMCMB1A225MTR
AX0003	Or P2+16P E/C3	C312 CU3035 Chip C	C1608JB1H1102KTA	C1608JB1F1E104ZTA
RG0173	Rubber PTT	C313 CU3035 Chip C	C1608JB1H102KTA	TMCMA1C105MTR
FG01792	Jack Cap DJG5	C314 CU3026 Chip C	C1608CH1H181JTA	C1608JB1H102KTA
FG01802	DC Cap DJG5	D301 X00299 Diode	C1608CH1H181JTA	C1608JB1H102KTA
WZ01812	Diar Cap DJG5	D302 X00293 Diode	C1608CH1H181JTA	C1608JB1H102KTA
FZ00207Y	Front Case	D303 X00129 Diode	C1608CH1H181JTA	C1608JB1H102KTA
NK0042	Dial Knob	D304 X00299 Diode	C1608CH1H181JTA	C1608JB1H102KTA
AB0012	S2.5+5FeNi1	I301 QA00120 Coil	C1608JB1H11226GR=R3	T.E.T.A
AB0012	0R2+3FeNi1	I301 QA00120 Coil	65/75BN-11226GR=R3	H2.I.F.H
AK0001	0R8+4FeNi	I301 QA00377A Coil	C1608CH1H181JTA	C1608JB1H102KTA
AN0012	Dial Nut	I302 Q00452 Coil	MJF1608AI0RKA00	C1608JB1H102KTA
RG0178Z	Jack Rubber DJG5	I303 Q00454 Coil	MJF1608K100KTA00	C1608JB1H102KTA
PM0100	Jack metal DJG5	I304 R00505 Coil	MJF1608K100KTA00	TMC5AC1C105MTR
AB00559	Rear Case DJ191	I305 R0137 Transistor	2SC5065-01(TP85)	C1608JB1H102KTA
TS0102	RF Shield DJ191	I306 R0137 Transistor	ZSC5065-01(TB85L)	T.E.T.A
IE00293A	RNC Receptacle	I307 XU0131 Chip R	DTC114EY T106	C1608CH1H220JTA
R3026	PTT Unit	R3026 Chip R	EP136SS1410V	C1608JB1H102KTA
R3030	DJG5 Flexible PCB	R3030 Chip R	EP136SS14221V	C1608CH1H102KTA
SM401	Switch S0P-1121ST	R3033 R03050 Chip R	EP136SS14103V	C1608JB1H102KTA
SM402	Switch TACTSW	R304 R03062 Chip R	EP136SS14104V	C1608CH1H102KTA
SM403	Switch S0P-1121ST	R305 R03062 Chip R	EP136SS14104V	C1608JB1H102KTA
SM404	Switch S0P-1121ST	R306 R03062 Chip R	EP136SS14104V	C1608CH1H102KTA
R308	JACK Unit	R307 R03052 Chip R	ERJ365T153V	C1608CH1H102KTA
R309	JACK Unit	R308 R03050 Chip R	ERJ365V14223V	C1608CH1H102KTA
R310	JACK Unit	R309 R03050 Chip R	ERJ365V1403V	C1608CH1H102KTA
R311	JACK Unit	R311 R03042 Chip R	ERJ365V14221V	C1608CH1H102KTA
TS0097	Case	TS0097 Case	ERJ365V14222V	C1608CH1H102KTA
U10030	SW Unit	U10030 Terminal	0.6 Pin	C1608CH1H102KTA
CN501	JACK Unit	CN501 U100255 Connector	6027D-037003	C1608CH1H102KTA
C504	Chip C	SH501 UU0018 Switch	SOP-1121ST	C1608CH1H102KTA
C505	Chip C	SH502 UU0018 Switch	SOP-1121ST	Switch space
C506	Chip C	SH502 FP0098		
JK901	Connector	C507 UU0019 Connector		
JK902	Connector	C508 UU0022 Connector		
I501	Connector	I501 HS1493-01-010 Coil		
C503	Chip C	I502 MJE3216A1ROM Coil		
C504	Chip C	I503 MJE3216A1ROM Coil		
C505	Chip C	I504 MJE3216A1ROM Coil		
I506	Connector	I505 MJE3216A1ROM Coil		
I507	Connector	I506 MJE3216A1ROM Coil		
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I613	Connector	I612 MJE3216A1ROM Coil		
I614	Connector	I613 MJE3216A1ROM Coil		
I615	Connector	I614 MJE3216A1ROM Coil		
I616	Connector	I615 MJE3216A1ROM Coil		
I617	Connector	I616 MJE3216A1ROM Coil		
I618	Connector	I617 MJE3216A1ROM Coil		
I619	Connector	I618 MJE3216A1ROM Coil		
I620	Connector	I619 MJE3216A1ROM Coil		
I621	Connector	I620 MJE3216A1ROM Coil		
I622	Connector	I621 MJE3216A1ROM Coil		
I623	Connector	I622 MJE3216A1ROM Coil		
I624	Connector	I623 MJE3216A1ROM Coil		
I625	Connector	I624 MJE3216A1ROM Coil		
I626	Connector	I625 MJE3216A1ROM Coil		
I627	Connector	I626 MJE3216A1ROM Coil		
I628	Connector	I627 MJE3216A1ROM Coil		
I629	Connector	I628 MJE3216A1ROM Coil		
I630	Connector	I629 MJE3216A1ROM Coil		
I631	Connector	I630 MJE3216A1ROM Coil		
I632	Connector	I631 MJE3216A1ROM Coil		
I633	Connector	I632 MJE3216A1ROM Coil		
I634	Connector	I633 MJE3216A1ROM Coil		
I635	Connector	I634 MJE3216A1ROM Coil		
I636	Connector	I635 MJE3216A1ROM Coil		
I637	Connector	I636 MJE3216A1ROM Coil		
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I650	Connector	I649 MJE3216A1ROM Coil		
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I654	Connector	I653 MJE3216A1ROM Coil		
I655	Connector	I654 MJE3216A1ROM Coil		
I656	Connector	I655 MJE3216A1ROM Coil		
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I659	Connector	I658 MJE3216A1ROM Coil		
I660	Connector	I659 MJE3216A1ROM Coil		
I661	Connector	I660 MJE3216A1ROM Coil		
I662	Connector	I661 MJE3216A1ROM Coil		
I663	Connector	I662 MJE3216A1ROM Coil		
I664	Connector	I663 MJE3216A1ROM Coil		
I665	Connector	I664 MJE3216A1ROM Coil		
I666	Connector	I665 MJE3216A1ROM Coil		
I667	Connector	I666 MJE3216A1ROM Coil		
I668	Connector	I667 MJE3216A1ROM Coil		
I669	Connector	I668 MJE3216A1ROM Coil		
I670	Connector	I669 MJE3216A1ROM Coil		

Ref. No.	Parts No.	Description	Parts Name	Ver.	Ref. No.	Parts No.	Description	Parts Name	Ver.
D113	XU0130	Diode	DA204U Y106		R123	RK3026	Chip R	ER13GSYJ101V	
PL1J01	XC0018	Filter	CFM450E		R124	RK3022	Chip R	ER13GSYJ170V	
JK1J01	RD0108	JPM01R-01	S-AV28	T.E.T.	R126	RK3050	Chip R	ER13GSYJ103V	
JC1J01	XA0381	IC			R127	RK3052	Chip R	ER13GSYJ155V	
JC1J01	XA0421	IC			R128	RK3050	Chip R	ER13GSYJ103V	
JC1J02	IC0352	IC	PFO311	0A,1P0	R129	RK3050	Chip R	ER13GSYJ102V	
JC1J03	XA0385	IC	H6076GP		R130	RK3038	Chip R	ER13GSYJ102V	
JC1J04	XA0372	IC	H52221P-G00C		R131	RK3053	Chip R	ER13GSYJ103V	
JC1J05	XA0343	IC	MJC372W-HL		R132	RK3066	Chip R	ER13GSYJ103V	
JC1J05	XA0210	IC	NJ2070M T1		R135	RK3047	Chip R	ER13GSYJ102V	
L101	QCA016	Ceil	MJL3216A2424		R138	RK3038	Chip R	ER13GSYJ102V	
L102	QKA654	Ceil	MRI-5 3.5T 0.4		R140	RK3052	Chip R	ER13GSYJ103V	
L103	QKA654	Ceil	MRI-5 3.5T 0.4		R141	RK3030	Chip R	ER13GSYJ122V	
L104	QKA654	Ceil	MRI-5 3.5T 0.4		R143	RK3042	Chip R	ER13GSYJ103V	
L105	QCA030	Ceil	MJL6080R-01KTA00		R144	RK3050	Chip R	ER13GSYJ103V	
L106	QCA030	Ceil	MJL6080R-01KTA00		R145	RK3074	Chip R	ER13GSYJ103V	
L107	QKA754	Ceil	QKA754		R146	RK3074	Chip R	ER13GSYJ103V	
L108	QC0050	Ceil	MJL3216A4874		R147	RK3074	Chip R	ER13GSYJ103V	
L109	QA0071	Ceil	QA0071		R148	RK3060	Chip R	ER13GSYJ083V	
L110	QA0071	Ceil	QA0071		R149	RK3074	Chip R	ER13GSYJ105V	
L111	QA0071	Ceil	QA0071		R150	RK3034	Chip R	ER13GSYJ107V	
L112	QA0071	Ceil	QA0071		R153	RK3054	Chip R	ER13GSYJ122V	
L113	QC0009	Ceil	MJL3216A10M		R154	RK3042	Chip R	ER13GSYJ103V	
L114	QC0430	FET	MJL6080R-01KTA00		R155	RK3058	Chip R	ER13GSYJ173V	
Q101	XT0119	Transistor	2SC3356-T10RZ1		R156	RK3058	Chip R	ER13GSYJ1182V	
Q102	XT0119	Transistor	2SC3356-T1BR24		R157	RK3041	Chip R	ER13GSYJ182V	
Q103	XU0172	Transistor	XPA01-TX		R158	RK3059	Chip R	ER13GSYJ1563V	
Q105	XU0096	Transistor	2SC4099-TX		R159	RK3047	Chip R	ER13GSYJ362V	
Q106	XE0020	FET	2SK3601GE TL	T.E.T.	R160	RK3042	Chip R	ER13GSYJ222V	
Q106	XE0009	FET	2SK3020GR		R161	RK3052	Chip R	ER13GSYJ153V	
Q107	XU0137	Transistor	2SC5065-0(TE851)		R162	RK3052	Chip R	ER13GSYJ153V	
Q108	XU0096	Transistor	2SC4099 T100N		R163	RK3030	Chip R	ER13GSYJ122V	
Q109	XU0093	Transistor	YPA01-TX		R164	RK3058	Chip R	ER13GSYJ173V	
Q110	XU0086	Transistor	2SA1213Y TE12L		R165	RK3046	Chip R	ER13GSYJ172V	
Q111	XU0088	Transistor	2SA1213Y TE12L		R166	RK3046	Chip R	ER13GSYJ1102V	
Q112	XU0027	Transistor	FM47KT 148		R167	RK3038	Chip R	ER13GSYJ122V	
Q113	XU0125	Transistor	YPA01-TX		R168	RK3038	Chip R	ER13GSYJ133V	
Q114	XU0088	Transistor	2SA1213Y TE12L		R169	RK3032	Chip R	ER13GSYJ102V	
Q115	XU0085	Transistor	2SC4081 T106R		R170	RK3038	Chip R	ER13GSYJ102V	
Q116	XU0172	Transistor	XP1501-TX		R171	RK3058	Chip R	ER13GSYJ172V	
Q117	XU0137	Transistor	2SC5065-0(TE851)		R172	RK3054	Chip R	ER13GSYJ1722W	
Q118	XU0125	Transistor	DT144MEUT106		R173	RK3041	Chip R	ER13GSYJ133V	
Q119	XU0038	Transistor	UN2214 TX		R174	RK3056	Chip R	ER13GSYJ532V	
Q120	XU0062	Transistor	YPA111 TX		R175	RK3054	Chip R	ER13GSYJ1223V	
R101	RK3028	Chip R	ER13GSYJ103V		R176	RK3046	Chip R	ER13GSYJ1473V	
R107	RK3046	Chip R	ER13GSYJ151V		R177	RK3070	Chip R	ER13GSYJ1472W	
R116	RK3026	Chip R	ER13GSYJ101V		R178	RK3054	Chip R	ER13GSYJ182V	
R103	RK3026	Chip R	ER13GSYJ101V		R179	RK3056	Chip R	ER13GSYJ133V	
R104	RK3034	Chip R	ER13GSYJ101V		R180	RK3042	Chip R	ER13GSYJ1722V	
R105	RK3046	Chip R	ER13GSYJ101V		R181	RK3046	Chip R	ER13GSYJ102V	
R106	RK3050	Chip R	ER13GSYJ103V		R182	RK3058	Chip R	ER13GSYJ1722V	
R107	RK3051	Chip R	ER13GSYJ103V		R183	RK3042	Chip R	ER13GSYJ1472W	
R113	RK3051	Chip R	ER13GSYJ123V		R184	RK3055	Chip R	ER13GSYJ103V	
R114	RK3050	Chip R	ER13GSYJ103V		R185	RK3052	Chip R	ER13GSYJ103V	
R115	RK3026	Chip R	ER13GSYJ103V		R186	RK3046	Chip R	ER13GSYJ104V	
R116	RK3050	Chip R	ER13GSYJ103V		R187	RK3058	Chip R	ER13GSYJ1722V	
R117	RK3028	Chip R	ER13GSYJ101V		R188	RK3050	Chip R	ER13GSYJ103V	
R111	RK3028	Chip R	ER13GSYJ101V		R189	RK3050	Chip R	ER13GSYJ103V	
R118	RK3050	Chip R	ER13GSYJ101V		R190	RK3050	Chip R	ER13GSYJ103V	
R119	RK3051	Chip R	ER13GSYJ103V		R192	RK3014	Chip R	ER13GSYJ104V	
R120	XU0062	Transistor	UN2214 TX		R193	RK3038	Chip R	ER13GSYJ102V	
R101	RK3028	Chip R	ER13GSYJ103V		R195	RK3056	Chip R	ER13GSYJ1533V	
R113	RK3051	Chip R	ER13GSYJ103V		R196	RK3052	Chip R	ER13GSYJ153V	
R118	RK3051	Chip R	ER13GSYJ102V		R198	RK3043	Chip R	ER13GSYJ1272V	
R121	RK3050	Chip R	ER13GSYJ103V		R203	RK3038	Chip R	ER13GSYJ102V	
R122	RK3030	Chip R	ER13GSYJ1221V		R204	RK3030	Chip R	ER13GSYJ1221V	

RF UNIT/E1-E28U

Ref. No.	Parts No.	Description	Parts Name	Ver.	Ref. No.	Parts No.	Description	Parts Name	Ver.
Eu-28U									
C701	RK3030	Chip R	ER13GSYJ221V		R205	RK3030	Chip R	ER13GSYJ103V	
C702	RK3036	Chip R	ER13GSYJ103V		R206	RK3039	Chip R	ER13GSYJ103V	
C703	RK3015	Chip R	ER13GSYJ103V		R209	RK3026	Chip R	ER13GSYJ103V	
C705	RK3015	Chip R	ER13GSYJ103V		R211	RK3062	Chip R	ER13GSYJ103V	
C706	RK3037	Chip R	ER13GSYJ103V		R212	RK3001	Chip R	ER13GSYJ103V	
C709	C5049	Chip R	ER13GSYJ103V		R213	RK3050	Chip R	ER13GSYJ103V	
C710	C5049	Chip R	ER13GSYJ103V		R214	RK3059	Chip R	ER13GSYJ103V	
C711	C5036	Chip R	ER13GSYJ103V		R215	RK3059	Chip R	ER13GSYJ103V	
C712	C5015	Chip R	ER13GSYJ103V		R216	RK3062	Chip R	ER13GSYJ103V	
C714	C5049	Chip R	ER13GSYJ103V		R219	RK3058	Chip R	ER13GSYJ103V	
C715	C5049	Chip R	ER13GSYJ103V		R220	RK3026	Chip R	ER13GSYJ103V	
Insulation Sheet 3AC 3.66640MHz									
AK2341									
X701	TZ0069				X701	XA2339			
CN701	UE0274				CN701	UP025A			
R718	RK3062				R718				
AK2341									
AXN320C038P E128U PCB									

ADJUSTMENT

1) Required Test Equipment

The following items are required to adjust radio parameters:

1. Regulated power supply	Supply voltage: Current:	5 - 14 VDC 3 A or more
2. Digital multimeter	Voltage range: Current: Input resistance:	FS = Approx. 20 V 10A or more High impedance
3. Oscilloscope	Measurable frequency:	Audio frequency
4. Audio dummy load	Impedance: Dissipation: Jack:	8 Ω 1 W or more 3.5 mm φ
5. SSG	Output frequency: Output level: Modulation:	200 MHz or more -20 dB/0.1 μ V - 120dB/1V AM/FM
6. Spectrum Analyzer	Measuring range:	Up to 2 GHz or more
7. Power meter	Measurable frequency: Impedance: Measuring range:	Up to 200 MHz 50 Ω , unbalanced 0.1 W - 10 W
8. Audio volmeter	Measurable frequency: Sensitivity:	Up to 100 kHz 1 mV to 10 V
9. Audio generator	Output frequency: Output impedance:	67 Hz to 10 kHz 600 Ω , unbalanced
10. Distortion meter /SINAD meter	Measurable frequency: Input level: Distortion level:	1 kHz Up to 40 dB 1 % - 100 %
11. Frequency counter	Measurable frequency: Measurable stability:	Up to 200 MHz Approx. +/-0.1 ppm
12. Linear detector	Measurable frequency: Characteristics: CN:	Up to 200 MHz Flat 60 dB or more

Note

- Standard modulation: 1 kHz +/-3.5 kHz/DEV
- Reference sensitivity: 12 dB SINAD
- Specified audio output level: 200 mW at 8 Ω
- Standard audio output level: 50 mW at 8 Ω
- Use an RF cable (3D2W: 1 m) for test equipment.
- Attach a fuse to the RF test equipment.
- All SSG outputs are indicated by EMF.
- Supply voltage for the transceiver: 13.8 VDC

2) Adjustment Mode

The DJ - 191 does not require a serviceperson to manipulate the components on the printed - circuit board, except the trimmer when adjusting reference frequency and deviation. Most of the adjustments for the transceiver are made by using the keys on it while the unit is in the adjustment mode. Because the adjustment mode temporarily uses the channels, frequency must be set on each channel before adjustments can be made. For instructions on how to program the channels, see the "DJ - 191 INSTRUCTION MANUAL" which came with the product. In consideration of the radio environment, the frequency on each channel must be near the value (+/- 1 MHz) listed in the table below. To enter the adjustment mode, turn the power off, hold down both the UP and DOWN keys, and press the POWER key. "chEc" appears on the LCD for about two seconds, and "C" appears indicating the unit is in the adjustment mode.

Channel frequencies used in the adjustment mode

Channel	Channel function	Frequency
1	Reference frequency adjustment	145 MHz
2	High power adjustment	145 MHz
3	Low power adjustment	145 MHz
4	Minimum frequency sensitivity adjustment	130 MHz
5	Medium frequency sensitivity adjustment	145 MHz
6	Maximum frequency sensitivity adjustment	173 MHz
7	S-meter (1) adjustment	145 MHz
8	S-meter (FULL) adjustment	145 MHz
9	Deviation	145 MHz
10	DTMF (1) test	145 MHz
11	DTMF (D) test	145 MHz
12	Tone 67 Hz test	145 MHz
13	Tone 88.5 Hz test	145 MHz
14	Tone 250.3 Hz test	145 MHz
15	Tone burst test	145 MHz
16	Aging (Not required to use)	145 MHz
20	VCO frequency shift change (Do not change).	-

Caution

- Do not press the **UP** or **DOWN** key while channel 20 is selected in the adjustment mode. Otherwise, the VCO switch frequency will change, causing a malfunction.

Reference Frequency Adjustment

1. In the adjustment mode, select channel 1 by rotating the main tuning dial.
2. Press the **PTT** key to start transmission.
3. Rotate TC101 on the RF circuit board until the value on the frequency counter matches the one displayed on the LCD.
4. On 145.05MHz measure TP near the VCO and adjust L301 to obtain $1.1V \pm 0.1V$ (If the second decimal point is flashing, the PLL is unlocked).

High Power Adjustment

1. In the adjustment mode, select channel 2 by rotating the main tuning dial.
2. Hold down the **F** key and press the **H/L** key to enter the high power mode ("L" at the lower-left of the display disappears).
3. Hold down the **PTT** key to start transmission.
4. While watching the reading of the TX power meter, set the output power to the value closest to 5 W by using the **UP** and **DOWN** keys.
5. When the **PTT** key is released, the output power at that time will be stored as the high power setting.

Low Power Adjustment

1. In the adjustment mode, select channel 3 by rotating the main tuning dial.
2. Hold down the **F** key and press the **H/L** key to enter the low power mode ("L" appears at the lower-left of the display).
3. Hold down the **PTT** key to start transmission.
4. While watching the reading of the TX power meter, set the output power to the value closest to 0.5 W by using the **UP** and **DOWN** keys.
5. When the **PTT** key is released, the output power at that time will be stored as the low power setting.

Minimum Frequency Sensitivity Adjustment

See "Note on Adjusting the Sensitivity" later in this section.

1. In the adjustment mode, select channel 4 by rotating the main tuning dial.
2. Using the **UP** and **DOWN** key, set the minimum frequency sensitivity.

Medium Frequency Sensitivity Adjustment

See "Note on Adjusting the Sensitivity" later in this section.

1. In the adjustment mode, select channel 5 by rotating the main tuning dial.
2. Using the **UP** and **DOWN** key, set the medium frequency sensitivity.

Maximum Frequency Sensitivity Adjustment

See "Note on Adjusting the Sensitivity" later in this section.

1. In the adjustment mode, select channel 6 by rotating the main tuning dial.
2. Using the **UP** and **DOWN** key, set the maximum frequency sensitivity.

S-meter (1) Adjustment

1. In the adjustment mode, select channel 7 by rotating the main tuning dial. The S-meter will show a single star (★).
2. Enter "0" dB μ (EMF) with the transceiver tester.
3. Press the **DOWN** key. The transceiver beeps indicating the new setting has been stored successfully.

**S-meter (FULL)
Adjustment**

1. In the adjustment mode, select channel 8 by rotating the main tuning dial. The S-meter will show all six stars (★ ★ ★ ★ ★ ★).
2. Enter "+20" dB μ (EMF) with the transceiver tester.
3. Press the **DOWN** key. The transceiver beeps indicating the new setting has been stored successfully.

Deviation

1. In the adjustment mode, select channel 9 by rotating the main tuning dial.
2. Input a 50 mVrms, 1 KMz signal with your transceiver tester through the external microphone jack.
3. With the tester, put the transceiver in the transmission mode.
4. Rotate the VR2 on the printed-circuit board of the transceiver until the deviation is set to 4.5 KHz.

DTMF (1) Test

This function is only for checking the DTMF code, not adjusting it.

1. In the adjustment mode, select channel 10 by rotating the main tuning dial.
2. Press the **PTT** key. DTMF code "1" is automatically sent and you will hear the monitoring tone from the speaker.
3. Check the deviation with the transceiver tester.

DTMF (D) Test

1. In the adjustment mode, select channel 11 by rotating the main tuning dial.
2. Press the **PTT** key. DTMF code "D" is automatically sent and you will hear the monitoring tone from the speaker.
3. Check the deviation with the transceiver tester.

Tone 67 Hz Test

This function is only for checking the tone encoder, not adjusting it.

1. In the adjustment mode, select channel 12 by rotating the main tuning dial.
2. Press the **PTT** key. A 67 Hz tone is automatically sent.
3. Check the deviation with the transceiver tester.

Tone 88.5 Hz Test

1. In the adjustment mode, select channel 13 by rotating the main tuning dial.
2. Press the **PTT** key. An 88.5 Hz tone is automatically sent.
3. Check the deviation with the transceiver tester.

Tone 250.3 Hz Test

1. In the adjustment mode, select channel 14 by rotating the main tuning dial.
2. Press the **PTT** key. A 250.3 Hz tone is automatically sent.
3. Check the deviation with the transceiver tester.

Tone Burst Test

This function is only for checking the tone burst, not adjusting it.

1. In the adjustment mode, select channel 15 by rotating the main tuning dial.
2. Press the **PTT** key. A 1750 Hz tone burst is automatically sent.
3. Check the deviation with the transceiver tester.

Aging

Perform this aging test only when necessary.

1. In the adjustment mode, select channel 16 by rotating the main tuning dial. The transceiver automatically repeats transmission for a minute and reception for another minute.

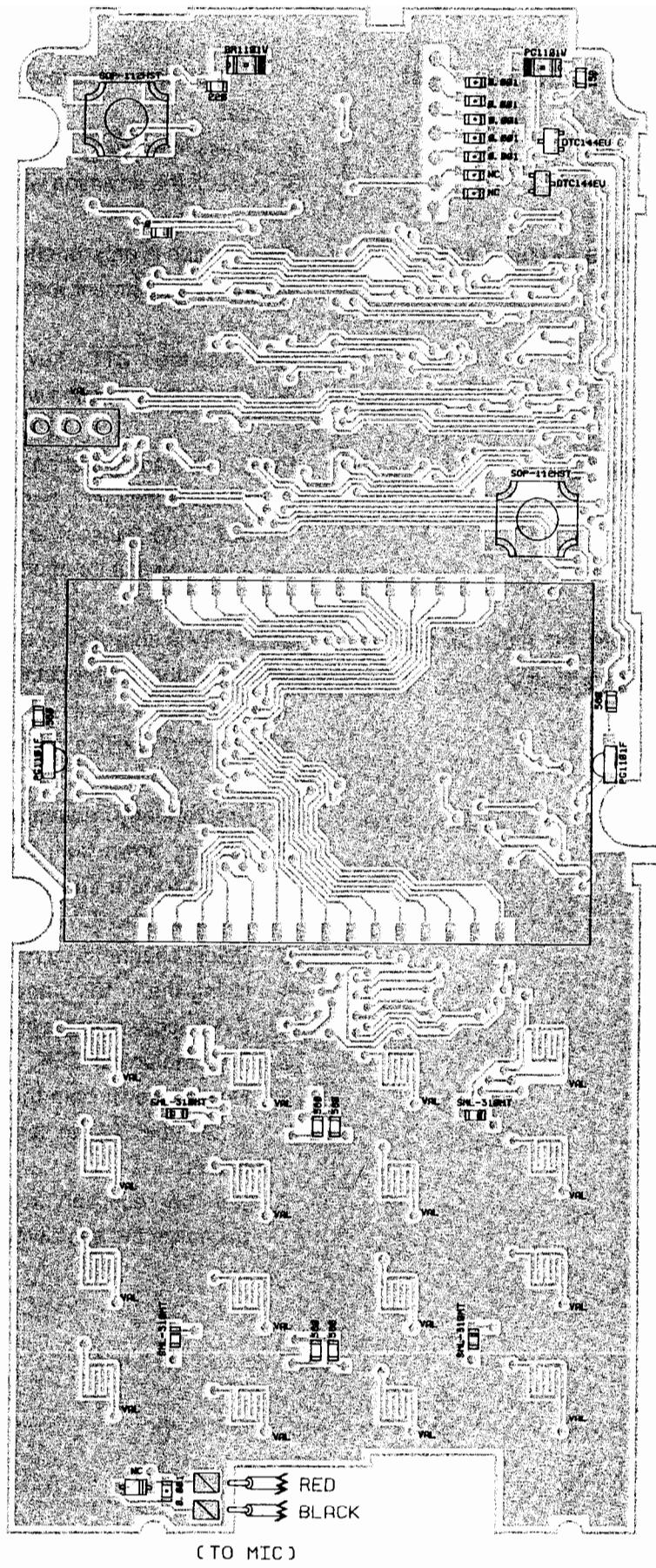
**Note on Adjusting
Sensitivity**

Sensitivity is adjusted by applying the optimum voltage from the CPU to the varicap of the tuning circuit. The coil manipulation for L109, L110, L111, and L112 is not required. If any of the coils is accidentally rotated, return it to the default position as described below, before adjusting the sensitivity.

1. Program any frequency within 145 MHz +/-1 on memory channel 5.
2. Holding down both the **UP** and **DOWN** key, press the POWER switch to turn the power ON. "chEc" will appear on the LCD for two seconds, and "C" appears.
3. Select channel 5 by rotating the main tuning dial.
4. Using the **UP** and **DOWN** keys, set the adjustment data to "7F" ("7F" appears in the channel number area on the LCD).
5. Turn the power OFF.
6. Holding down both the **UP** and **DOWN** key, turn the power ON. When the "C" no longer appears, the transceiver is in the normal status.
7. Set the reception frequency to 145 MHz +/-1. Rotate the coil to maximize the sensitivity.

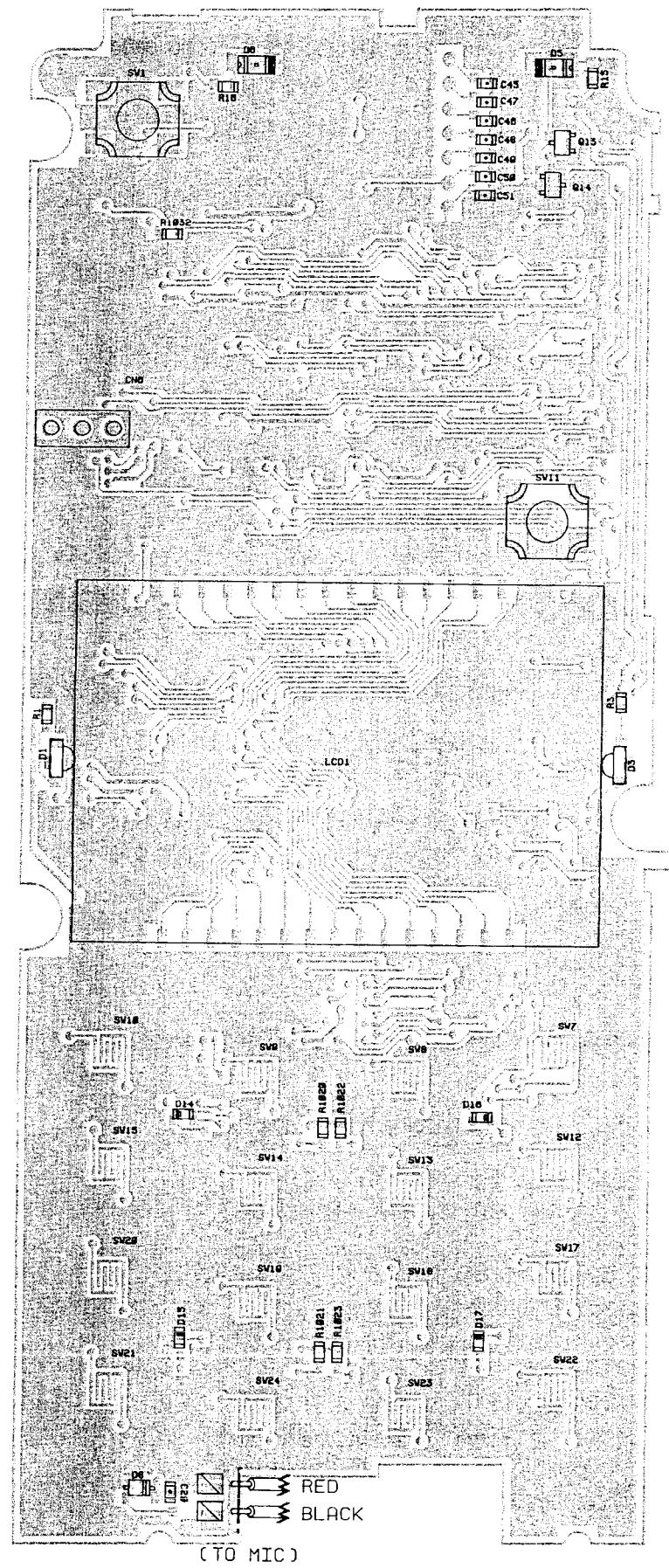
PC BOARD VIEW

CPU Unit Side A (VALUE)

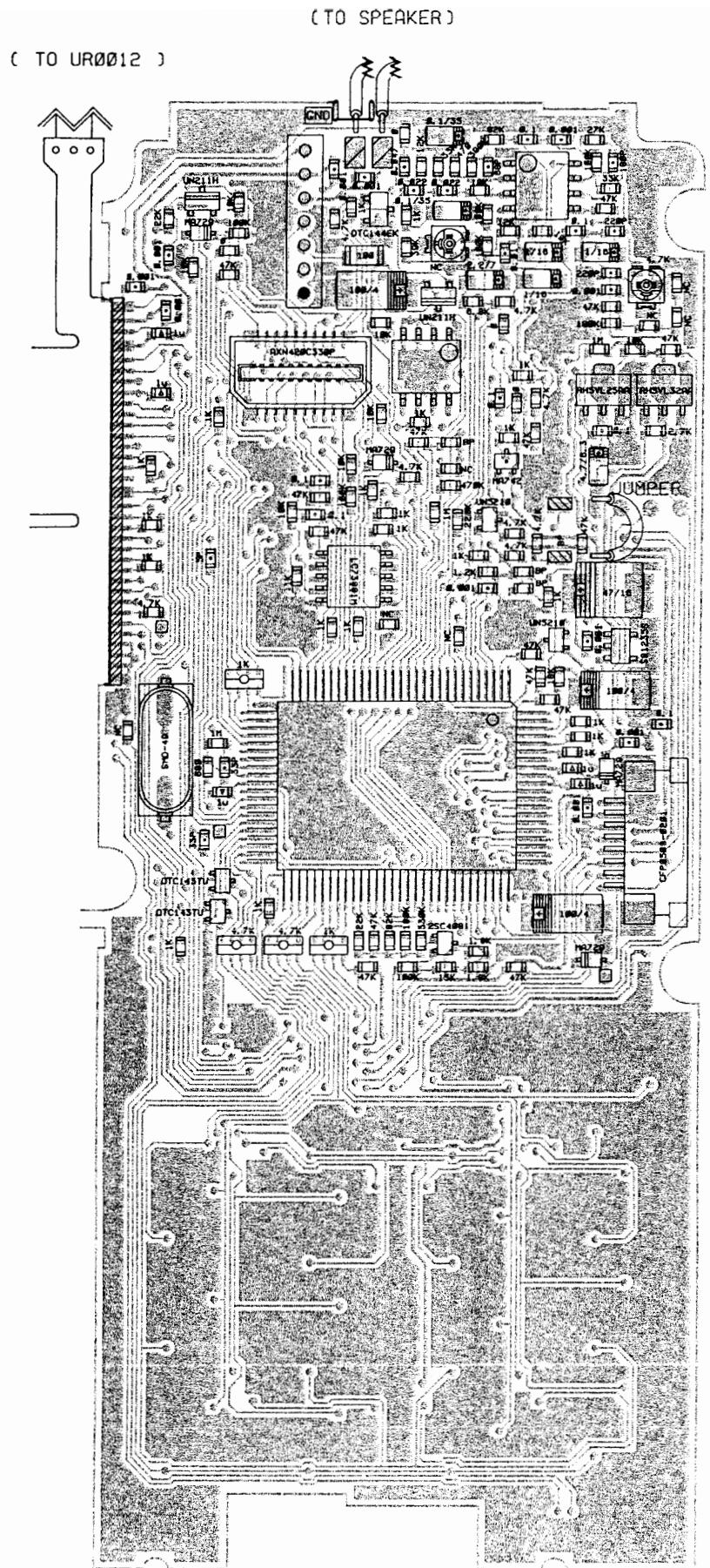


(TO MIC)

CPU Unit Side A (REFERENCE)



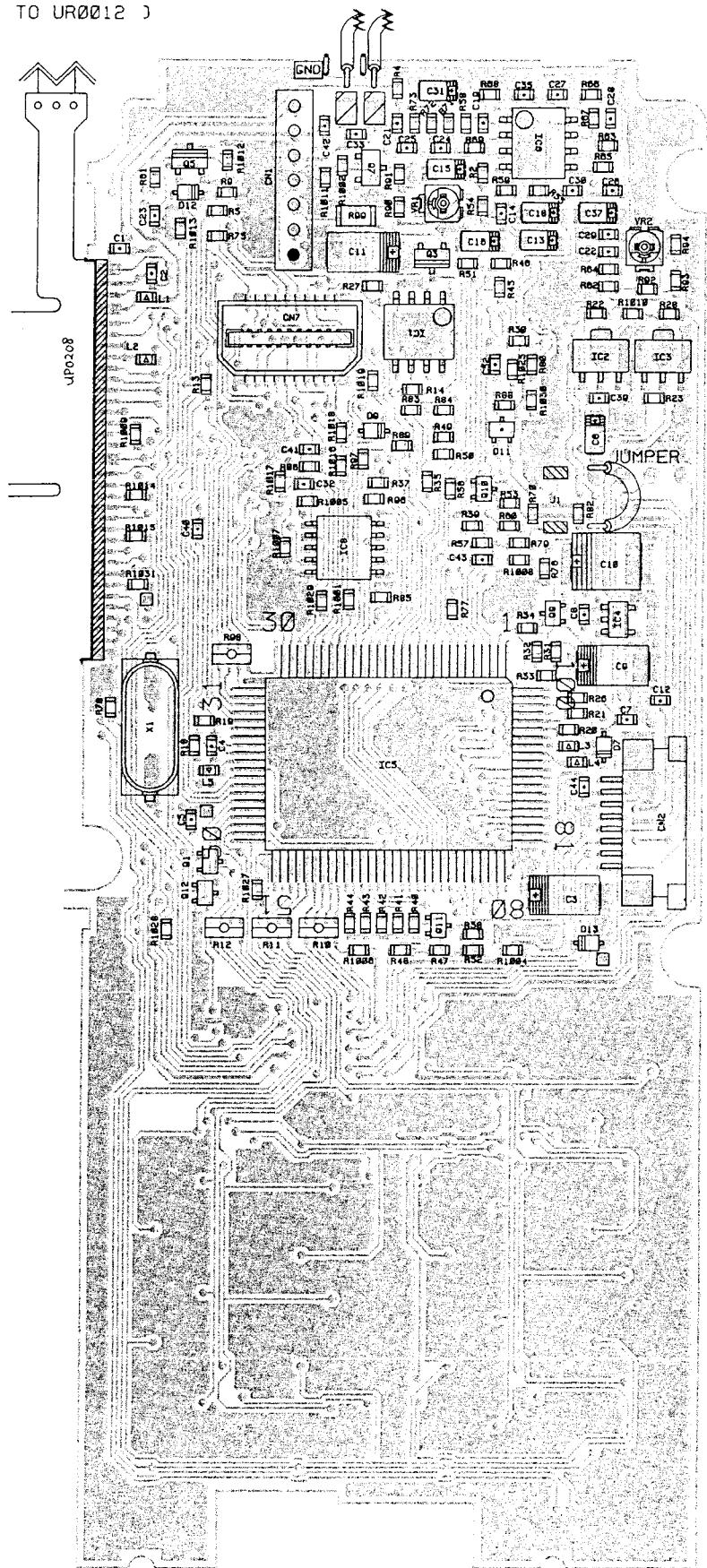
**CPU Unit Side B
(VALUE)**



	R79	R84	R1008	J1
T	—	—	—	JAMPER
TA	—	—	—	—
E	1K	1K	Ø	—

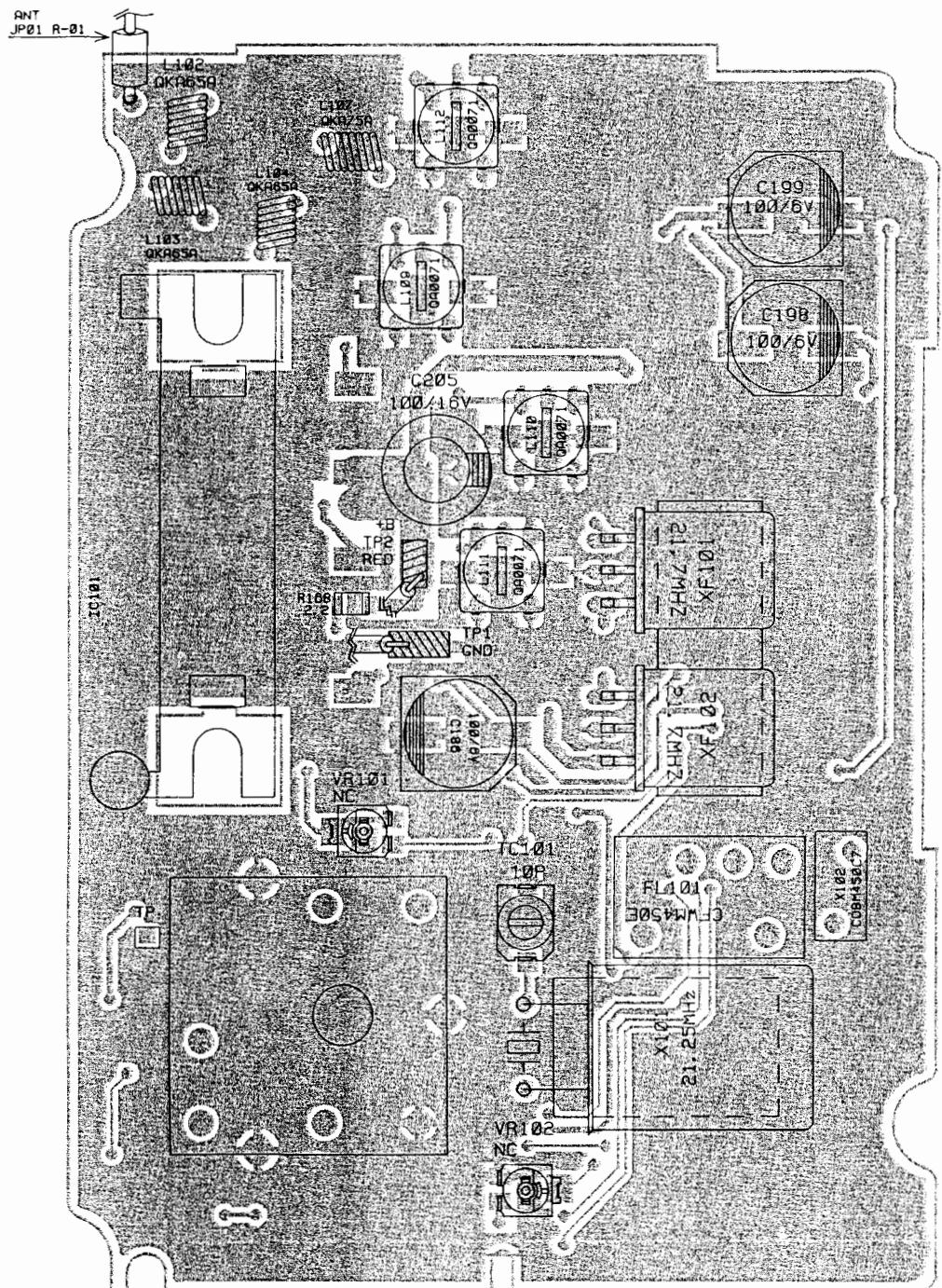
**CPU Unit Side B
(REFERENCE)**

(TO SPEAKER)
(TO UR0012)

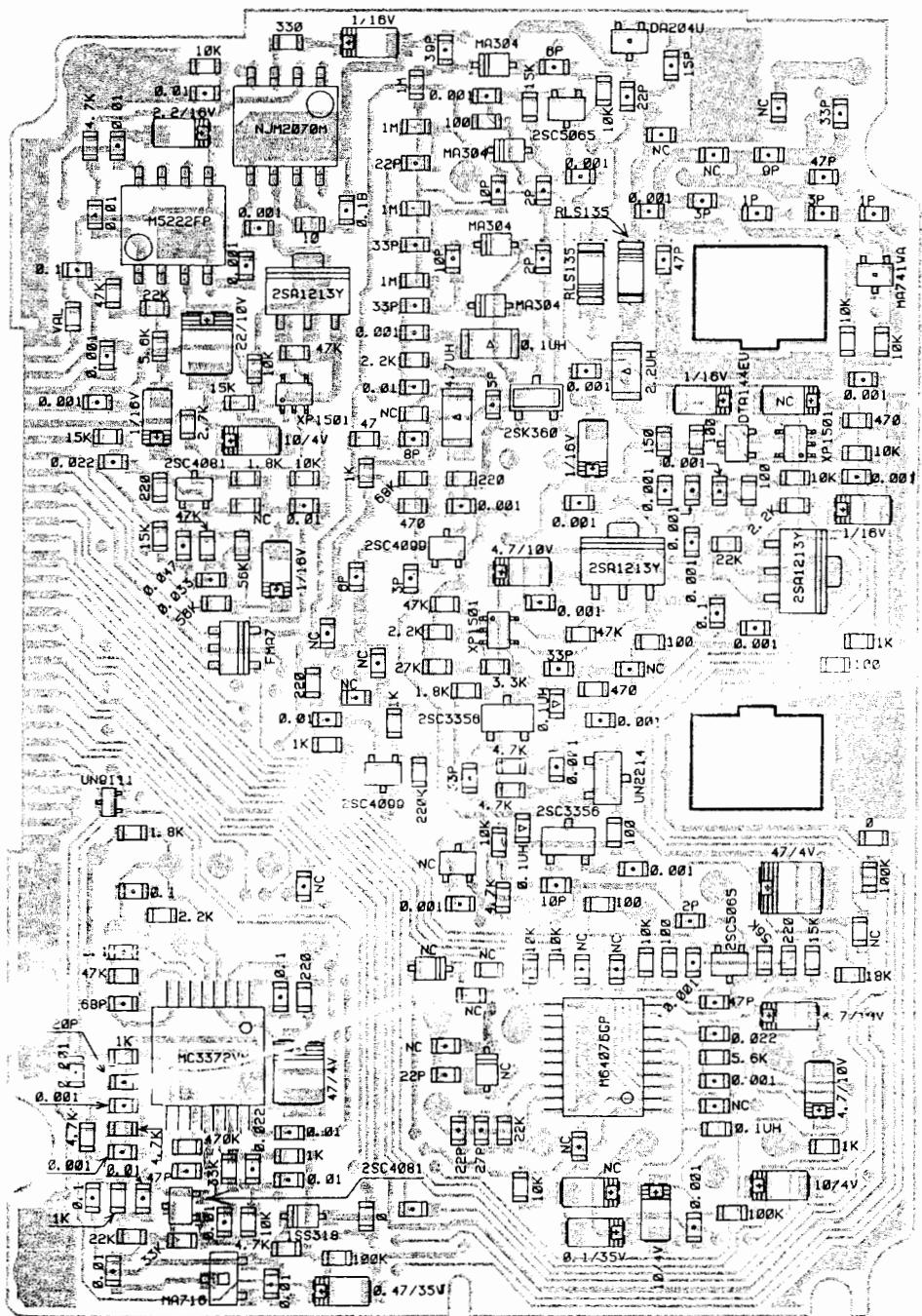


	R79	R84	R1008	J1
T	—	—	—	JAMPER
TA	—	—	—	—
E	1K	1K	Ø	—

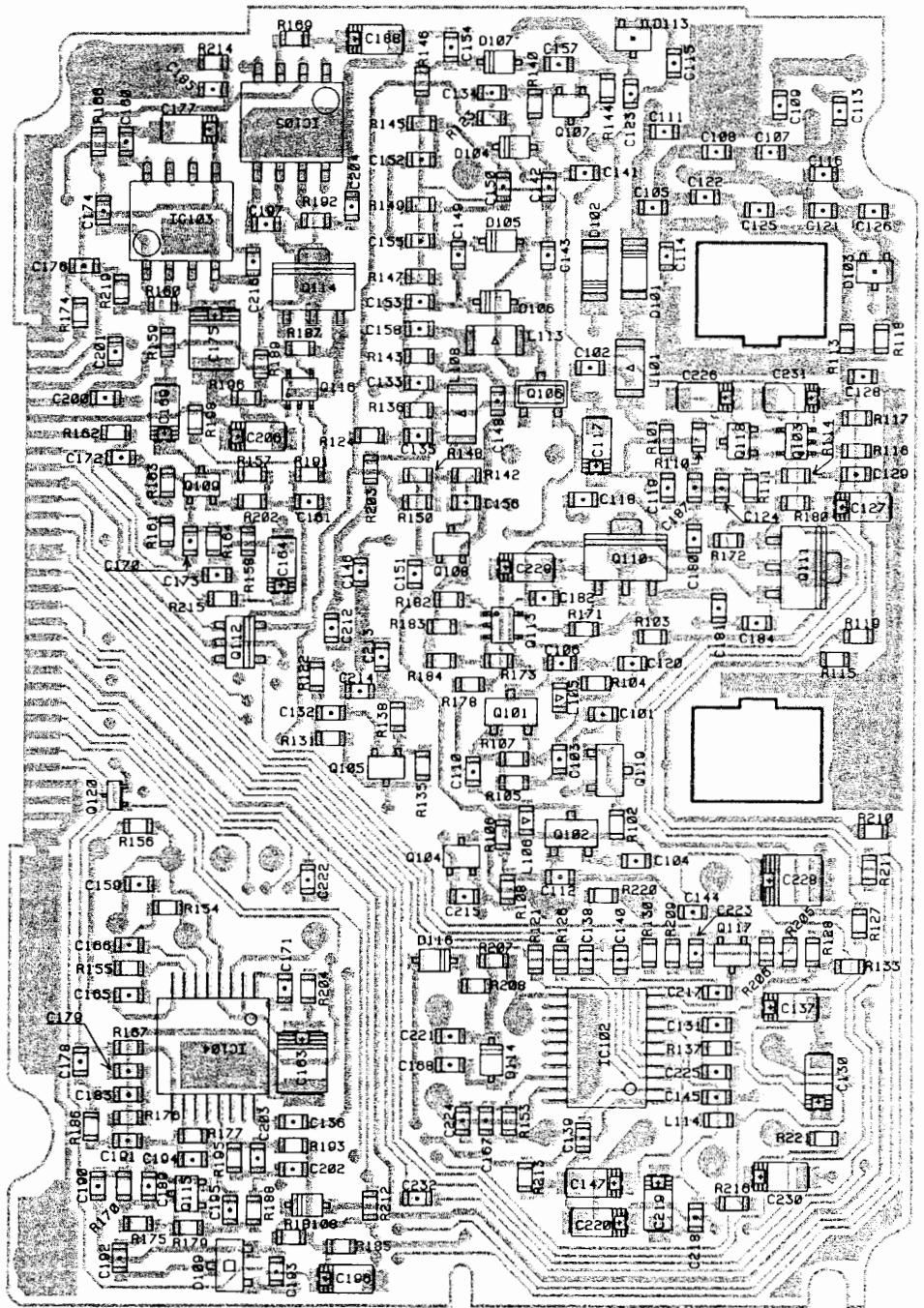
RF Unit Side A (VALUE / REFERENCE)



**RF Unit Side B
(VALUE)**

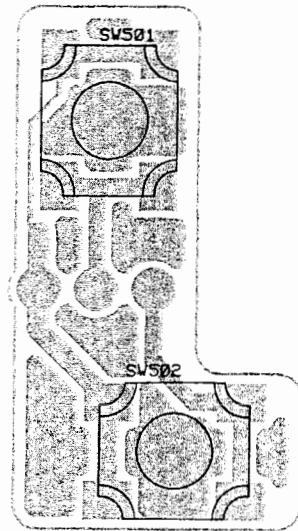
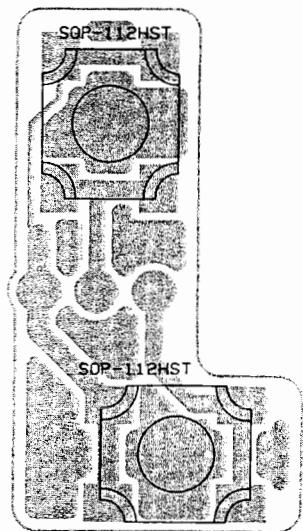


RF Unit Side B (REFERENCE)



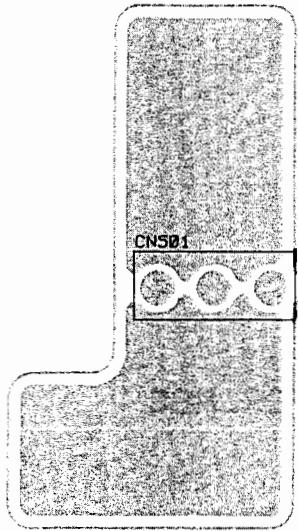
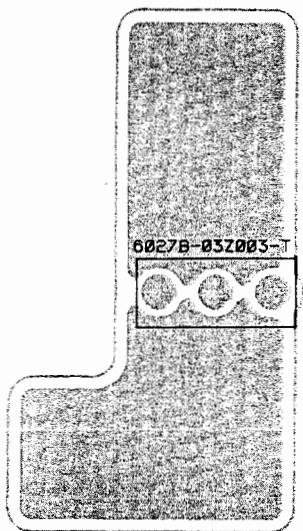
SW Unit Side A
(VALUE)

(REFERENCE)

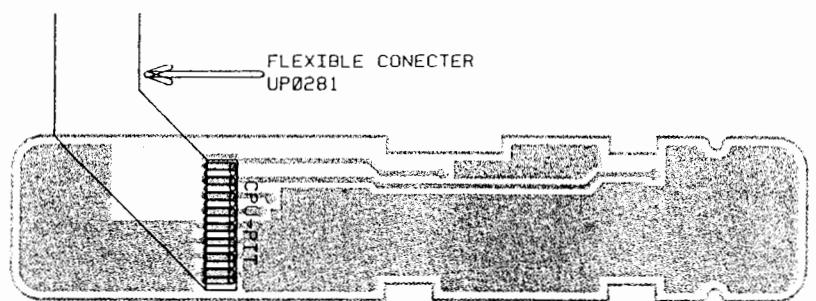


SW Unit Side B
(VALUE)

(REFERENCE)



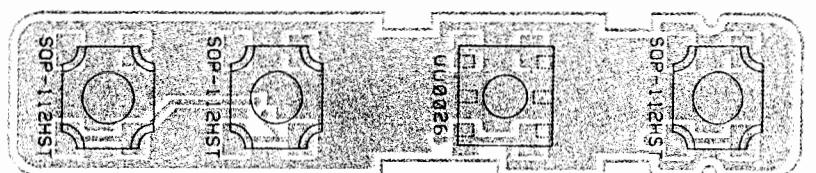
**PTT Unit Side A
(VALUE)**



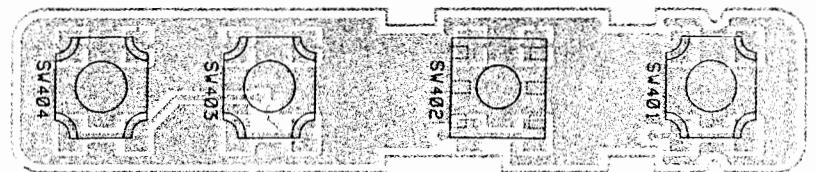
(REFERENCE)



**PTT Unit Side B
(VALUE)**

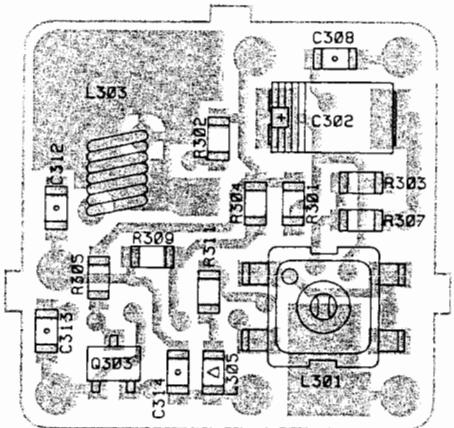
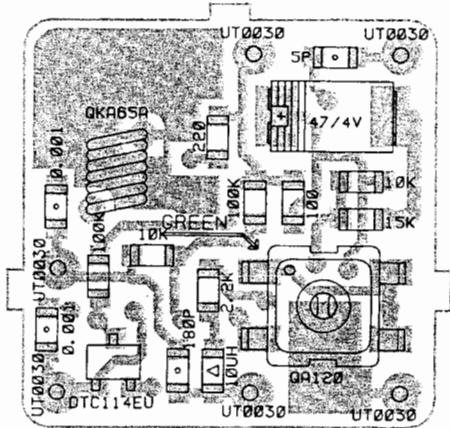


(REFERENCE)



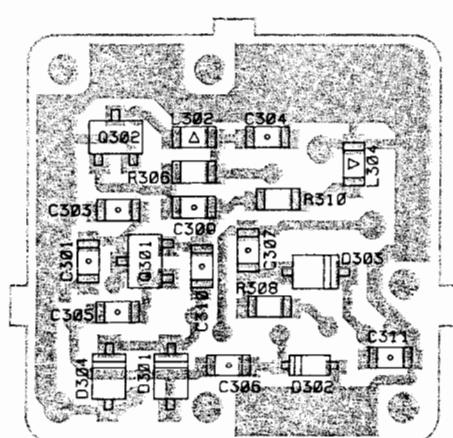
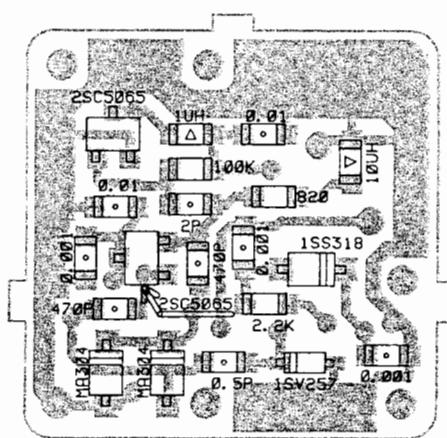
VCO Unit Side A (VALUE)

(REFERENCE)



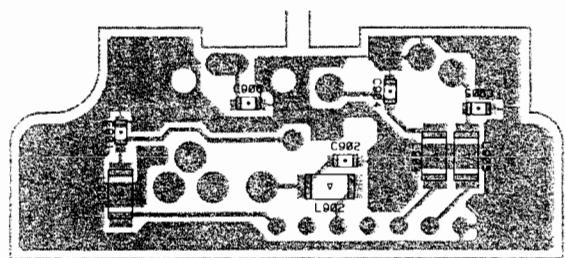
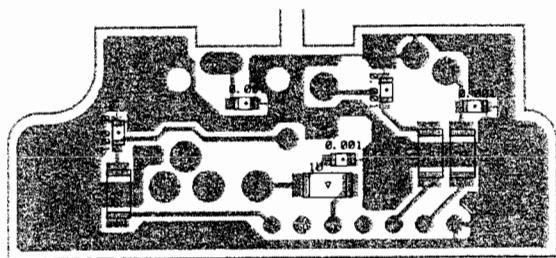
VCO Unit Side B (VALUE)

(REFERENCE)



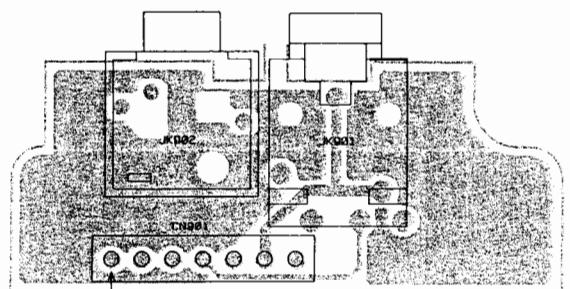
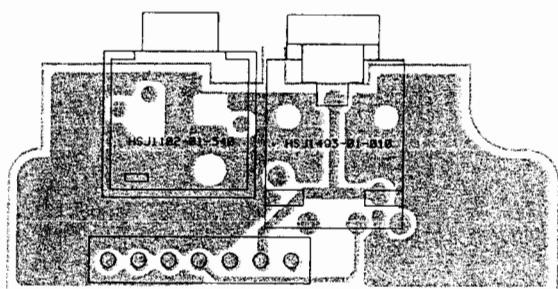
JACK Unit Side A
(VALUE)

(REFERENCE)



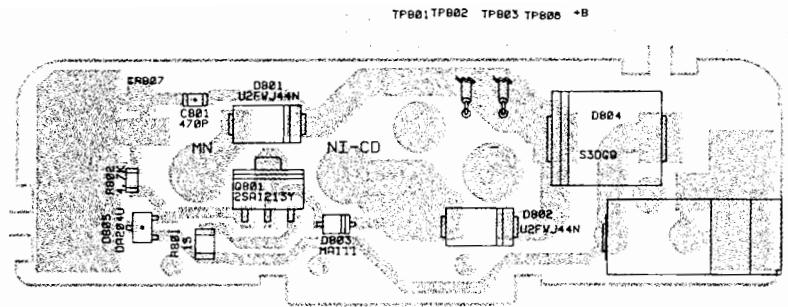
JACK Unit Side B
(VALUE)

(REFERENCE)

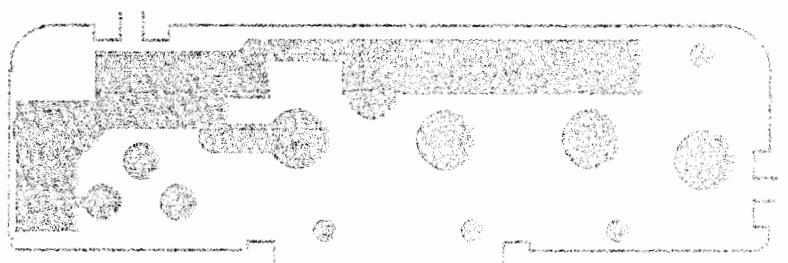


black

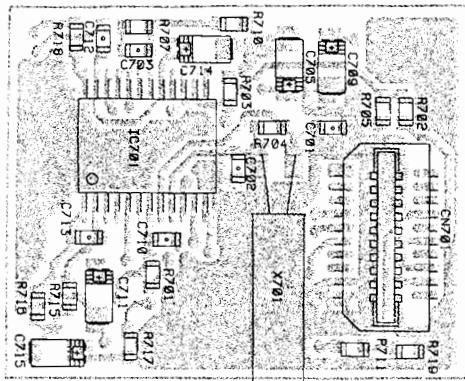
**CHARGE Unit Side A
(VALUE / REFERENCE)**



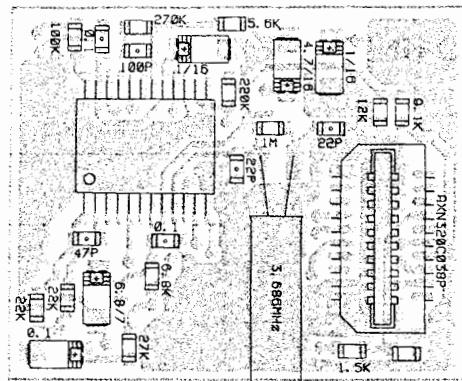
**CHARGE Unit Side B
(VALUE / REFERENCE)**



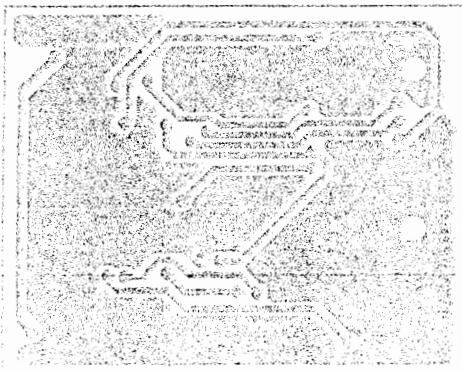
**TSQ UNIT Side A
(VALUE)**



(REFERENCE)

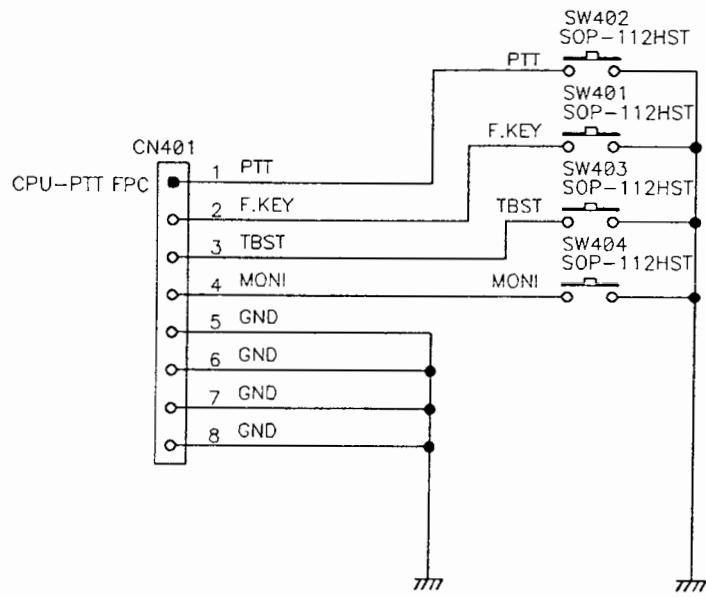


**TSQ UNIT Side B
(VALUE/REFERENCE)**

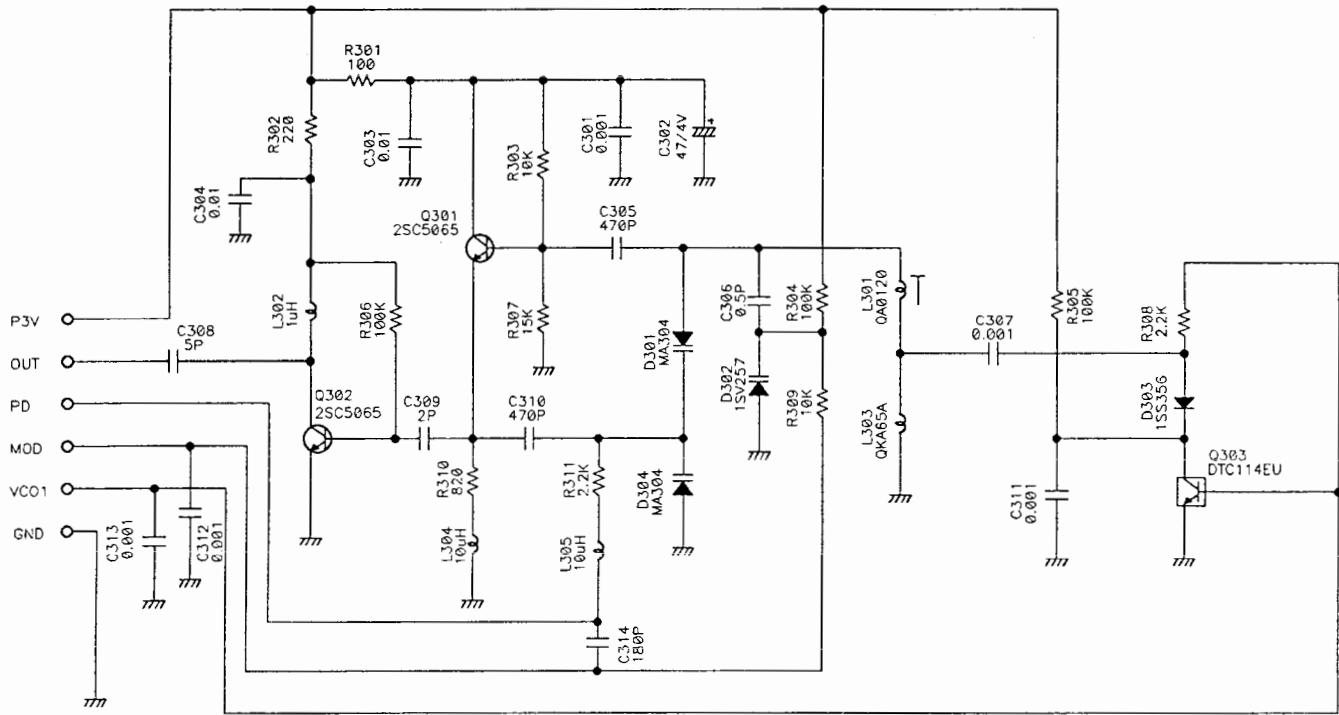


CIRCUIT DIAGRAM

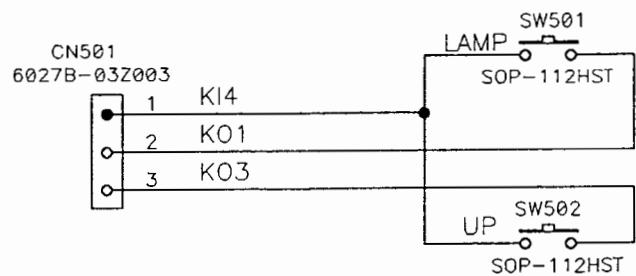
PTT UNIT



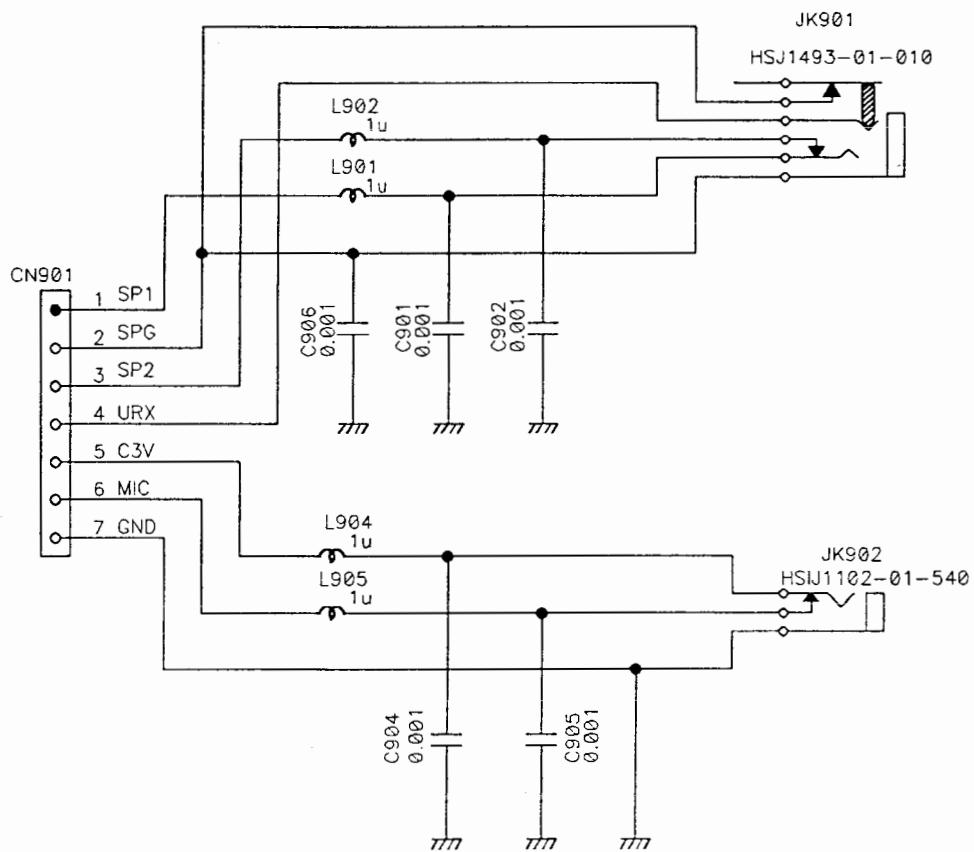
VCO UNIT



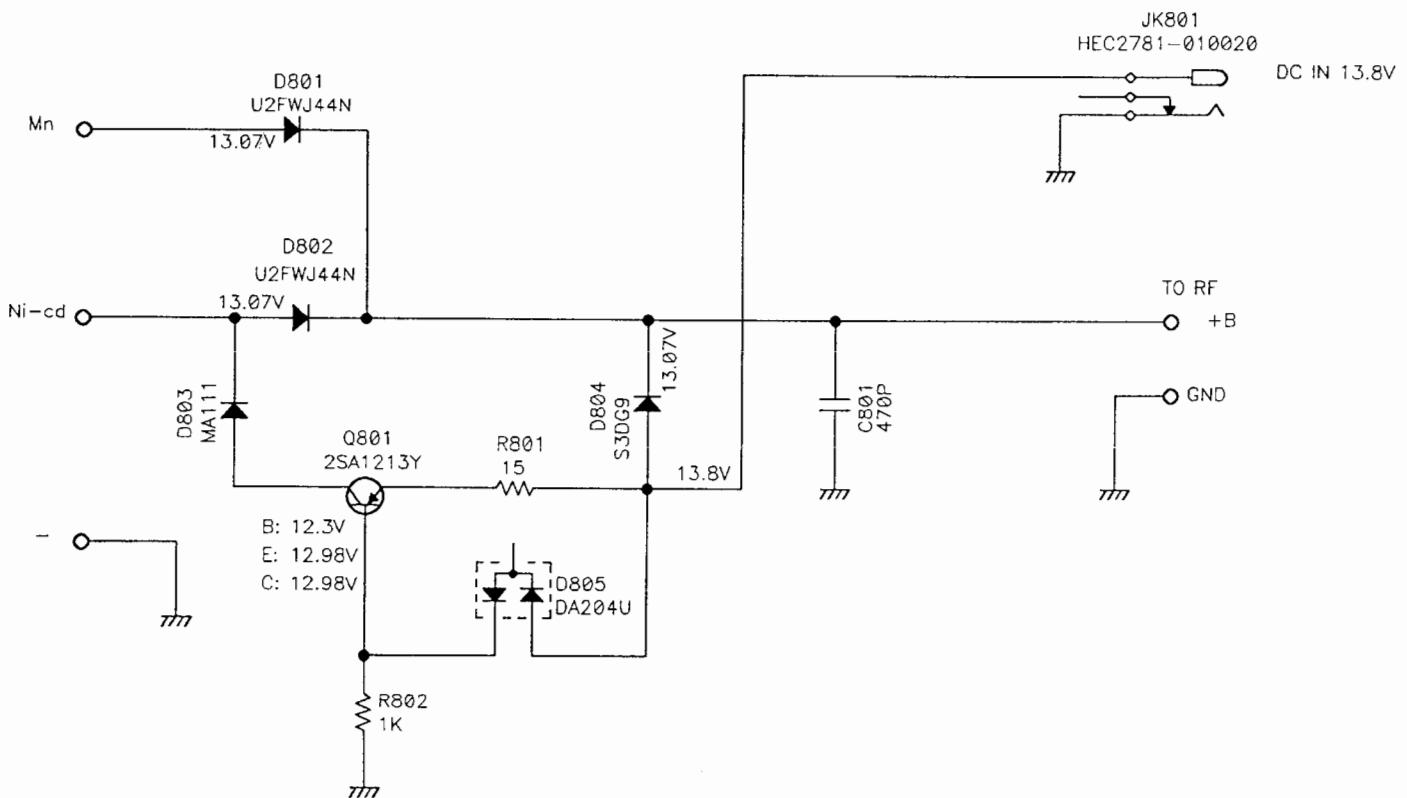
SW UNIT



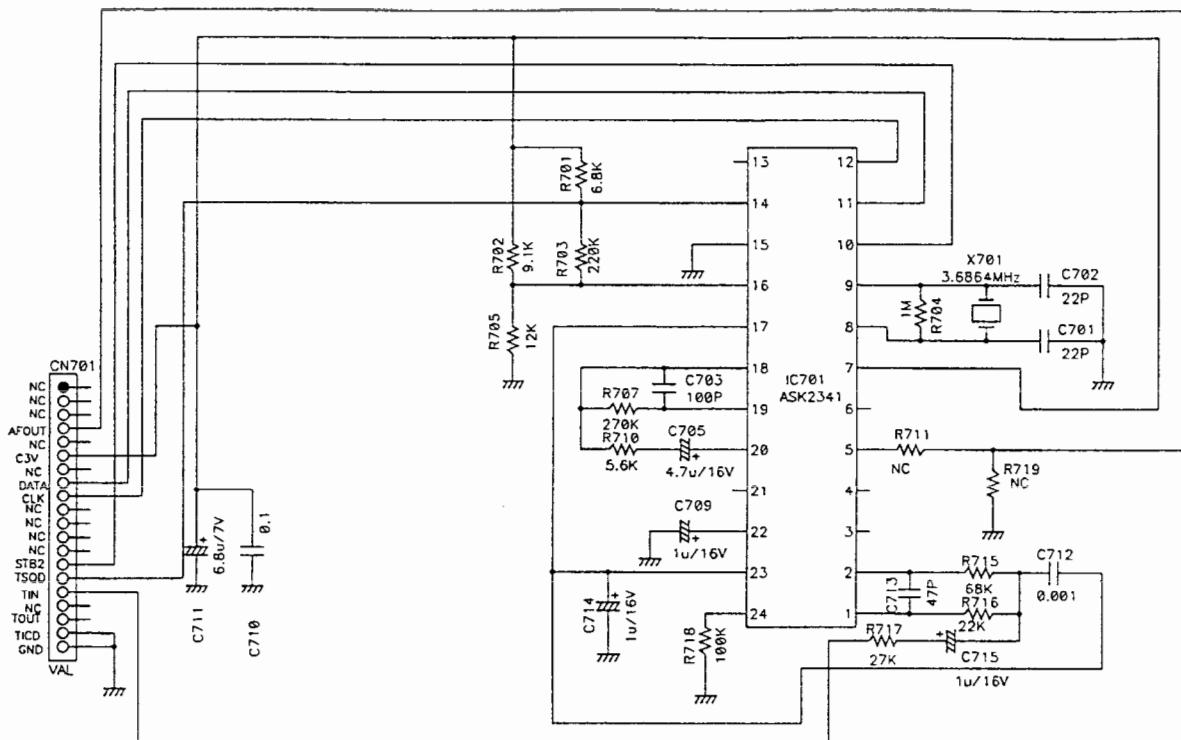
SP-JACK UNIT



CHARGE UNIT



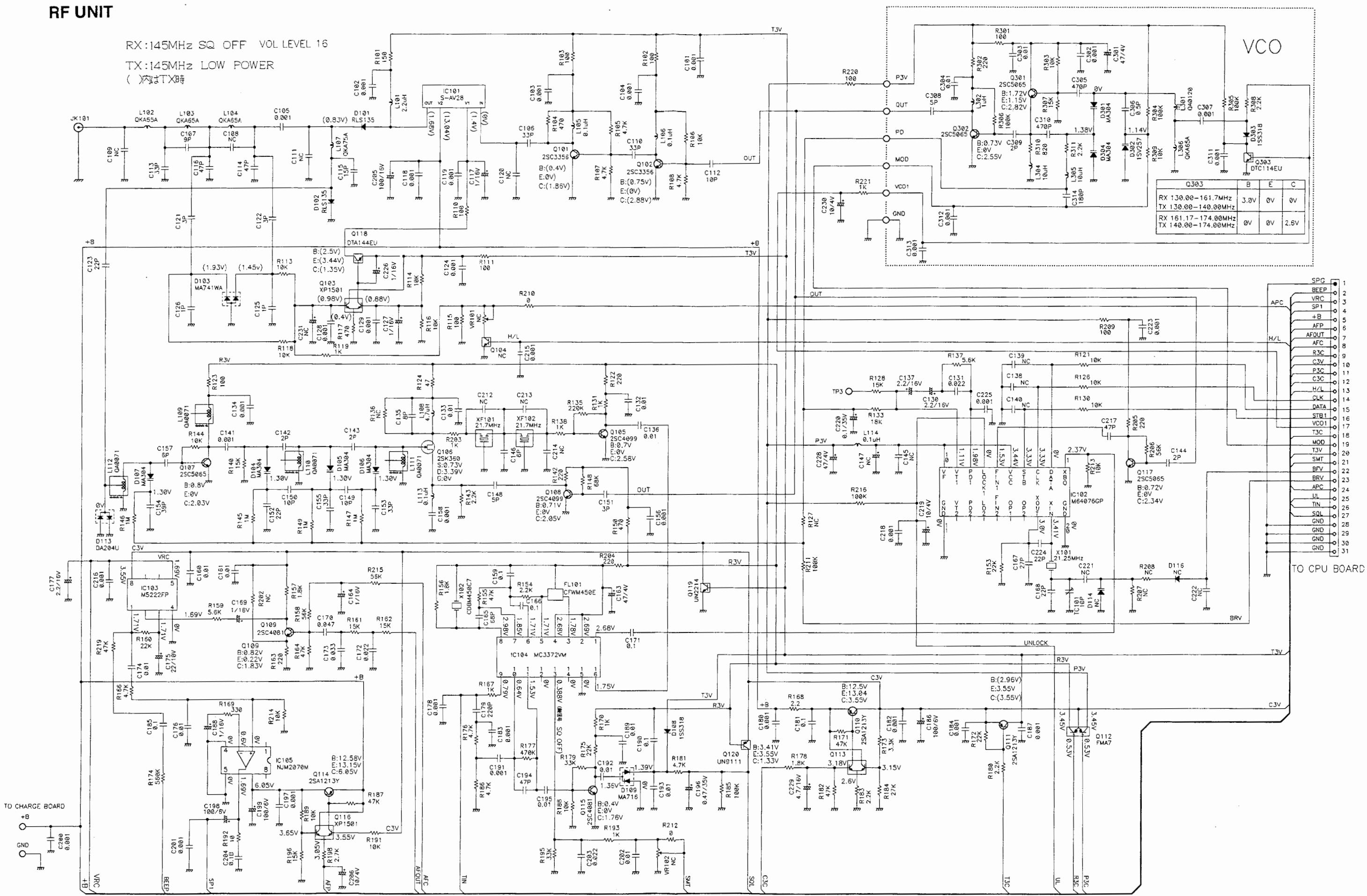
TSQ UNIT



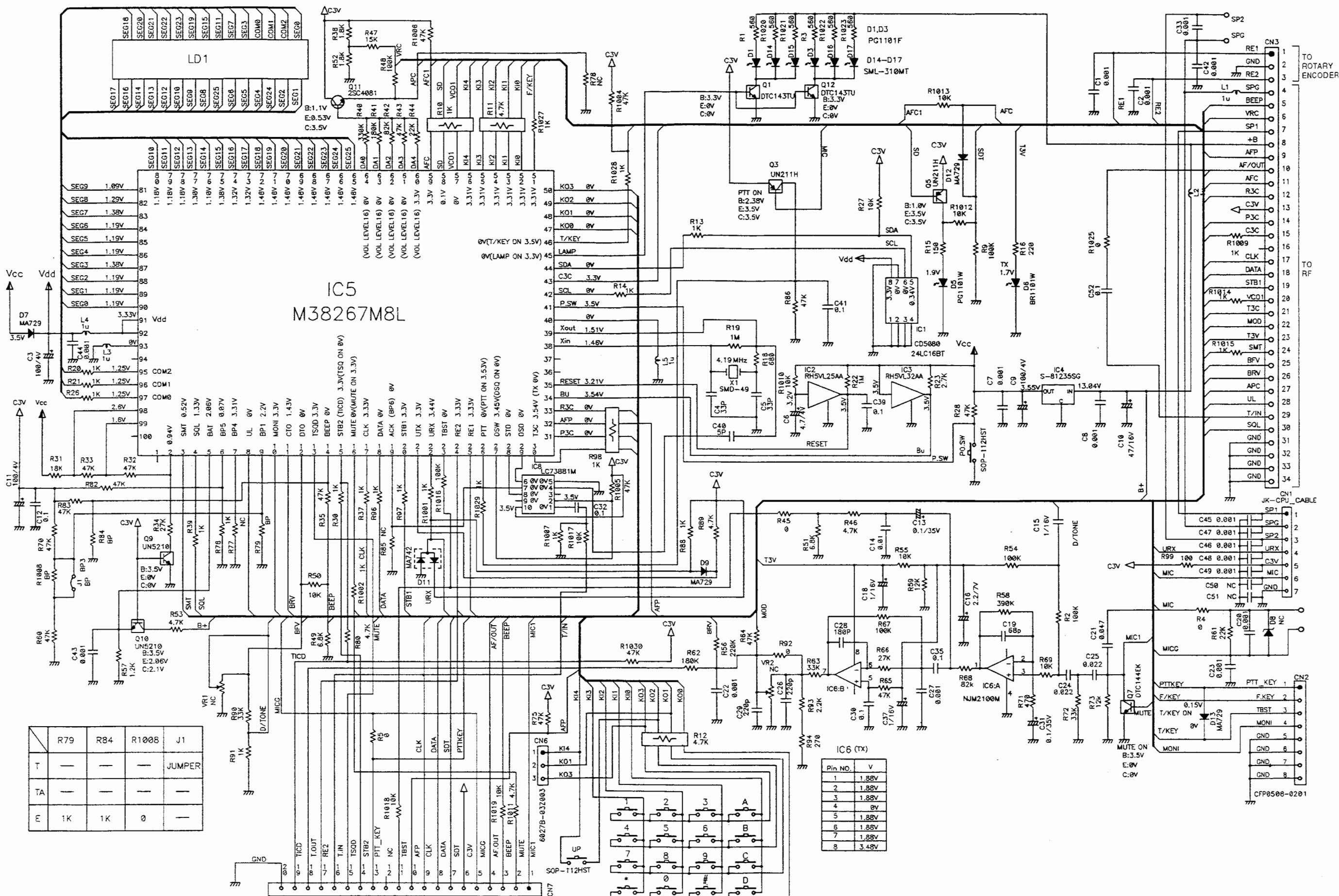
RF UNIT

RX:145MHz SQ OFF VOL LEVEL 16

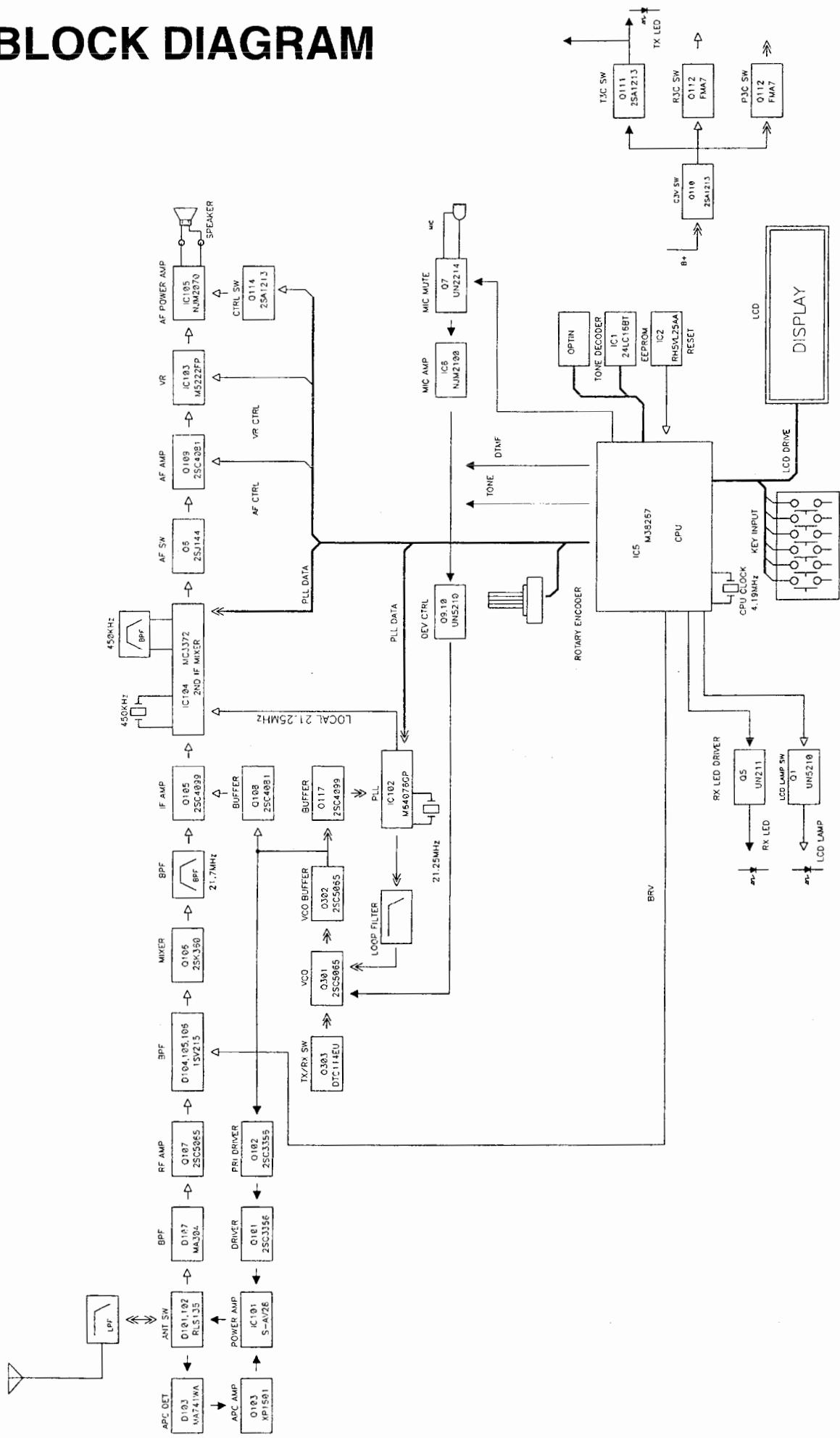
TX:145MHz LOW POWER
()内はTX時



CPU UNIT



BLOCK DIAGRAM



→ TRANSMIT
 ← RECEIVE
 ⇢ RECEIVE/TRANSMIT